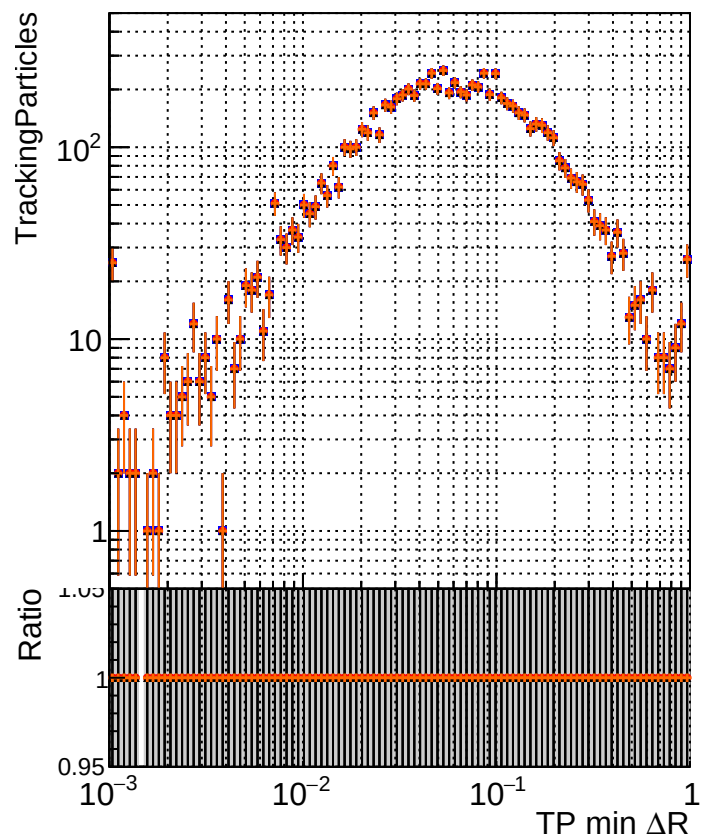
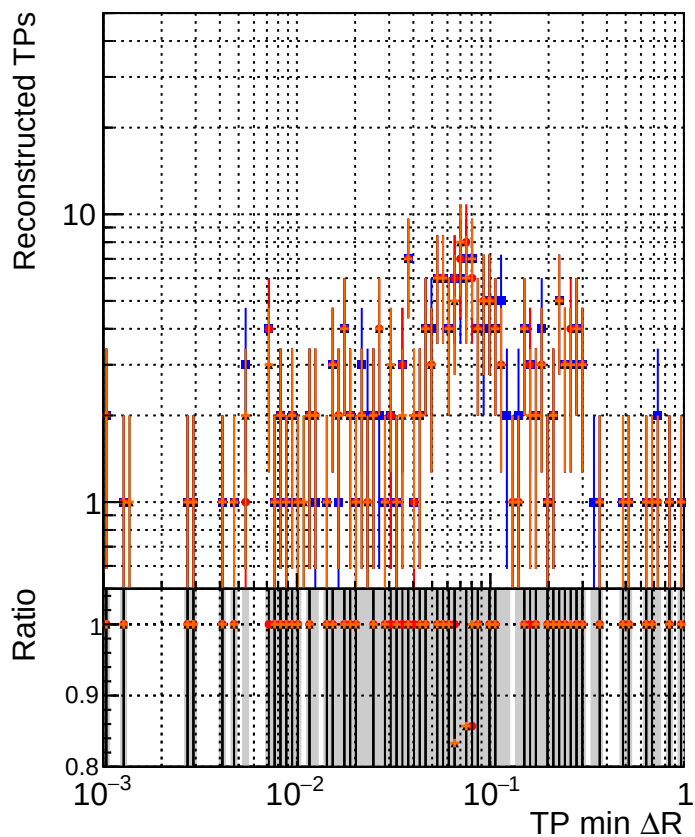


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated trac

■ DQM\_original4  
● DQM\_testMkFit\_initialStepReso  
▲ DQM\_testMkFitFit\_initialStepReso  
+ DQM\_testMkFitFit\_initialStepReso\_Chi2Oneway

