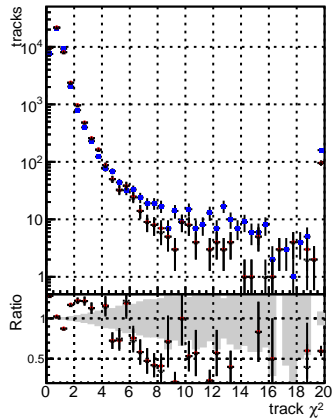
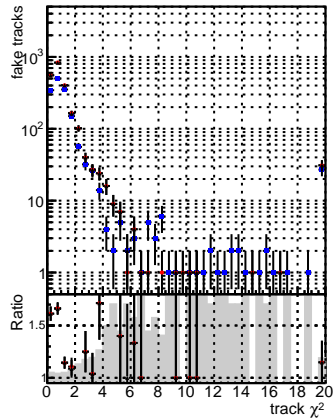
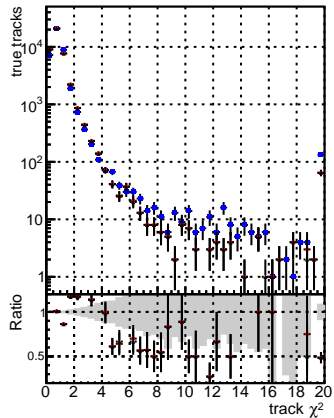


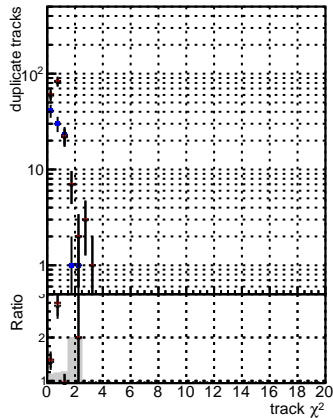
N of reco track vs normalized  $\chi^2$



N of associated (recoToSim) tracks vs normalized  $\chi^2$

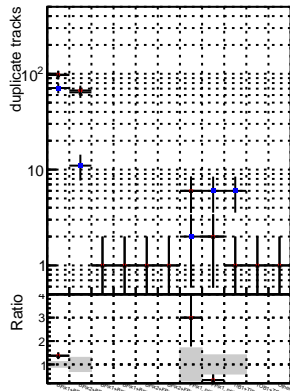
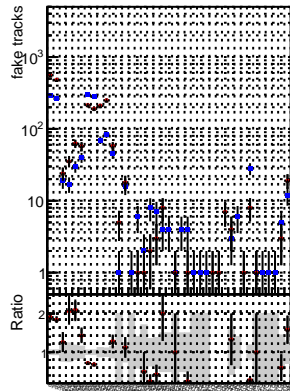
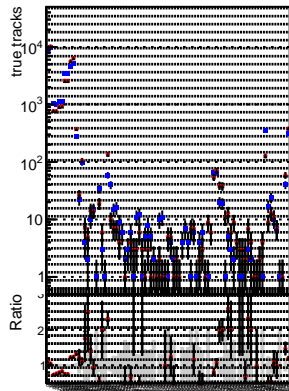
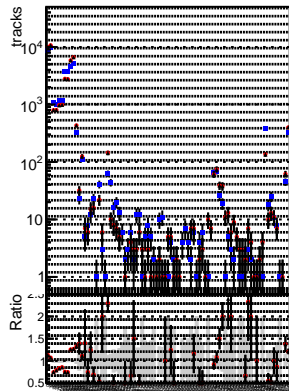


N of associated (recoToSim) loop tracks vs normalized  $\chi^2$



N of reco track vs.  $\chi^2$

DCM V0001	R0000000001	Global	CMSSW_41x502	RECO
DCM V0001	R0000000001	Global	CMSSW_mkt502	RECO
DCM V0001	R0000000001	Global	CMSSW_mkt503	RECO



overSet