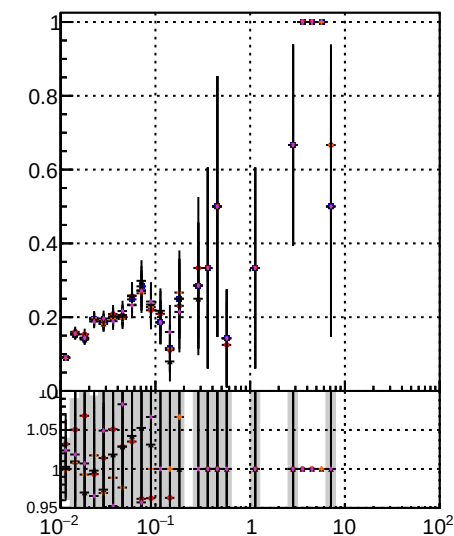
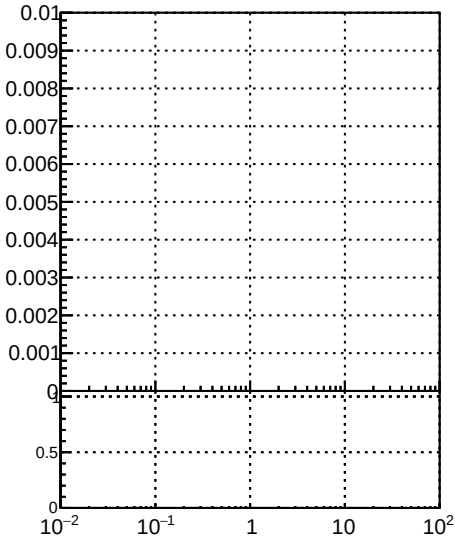


Fake rate vs vertpos



Duplicates Rate vs vertpos



Pileup rate vs vertpos

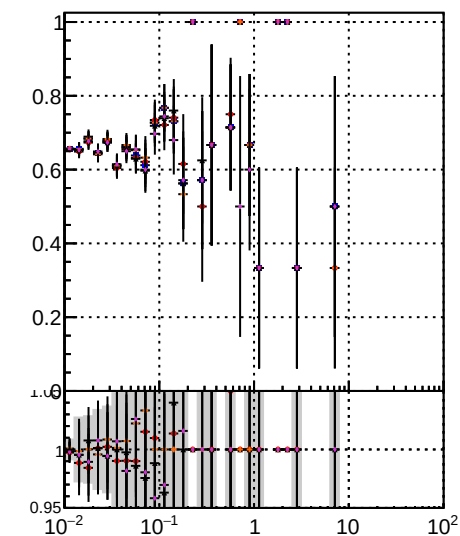
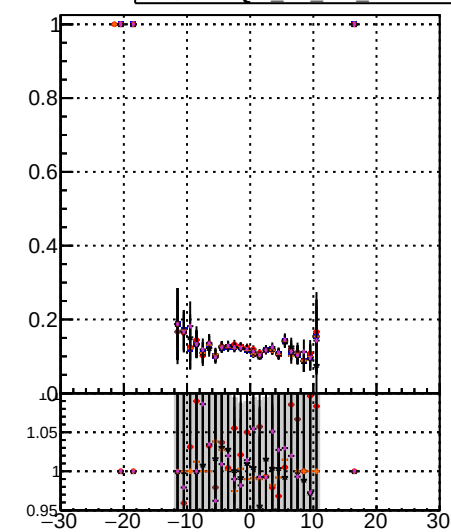
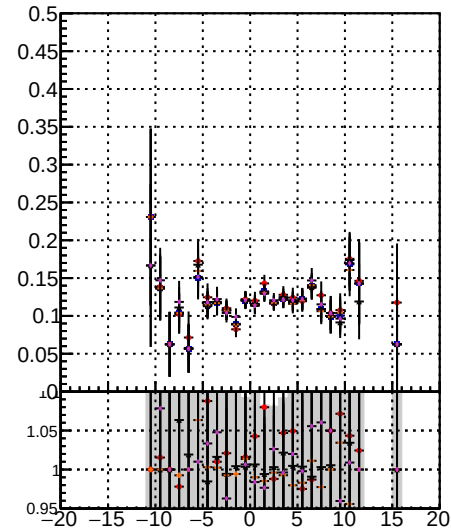


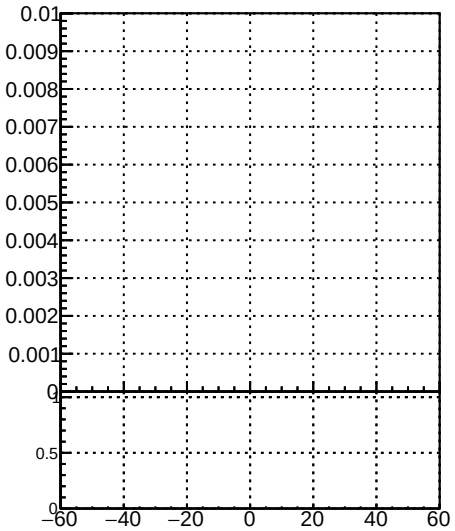
Diagram illustrating the Fake rate vs v workflow. The process involves a loop of steps: DOM, TT, MS, and init, which is repeated for both 'hot' and 'det' conditions.



Fake rate vs. sim PV z



Duplicates Rate vs sim PV z



Pileup rate vs. sim PV z

