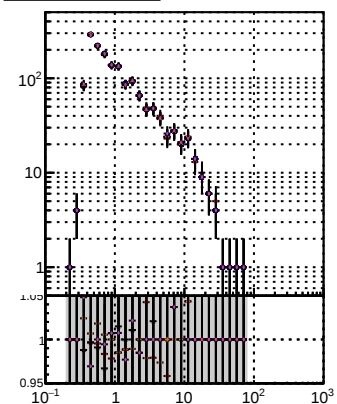
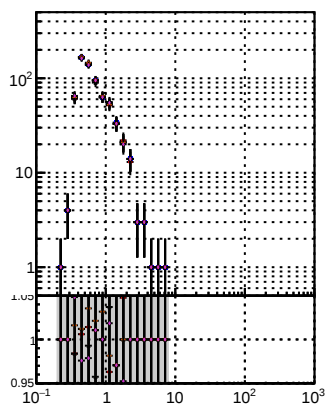
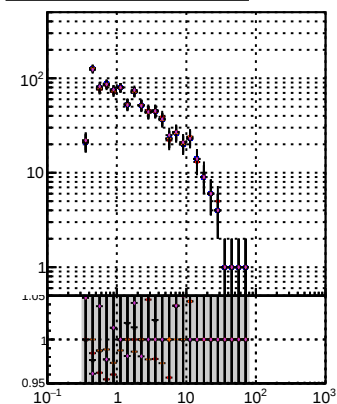


N of reco track vs pT



N of associated (recoToSim) tracks vs pT



N of associated (recoToSim) duplicate tracks vs pT

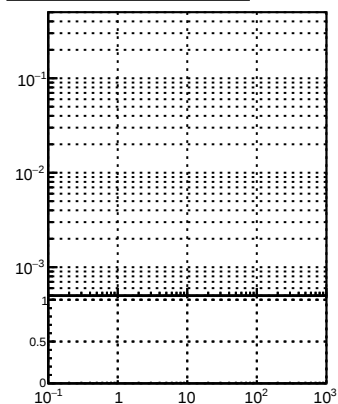
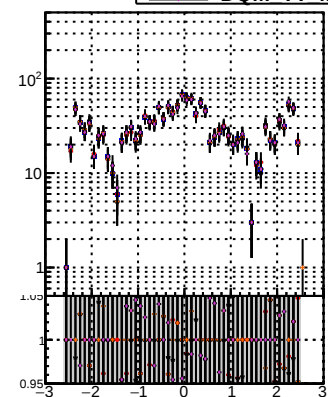
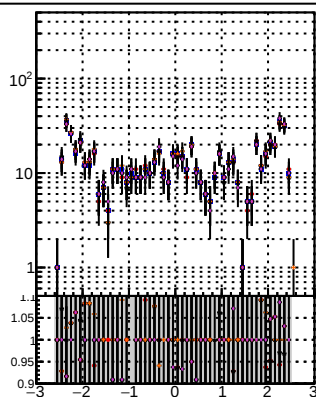
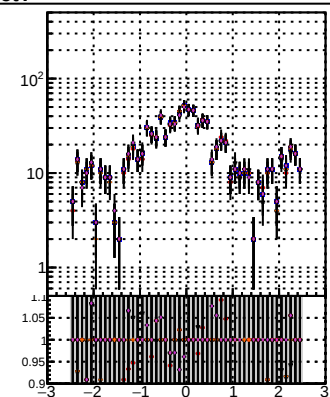


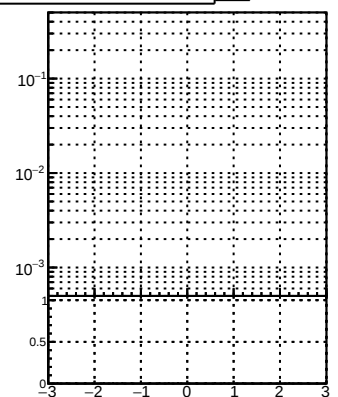
Diagram illustrating the detector layout, showing the sequence of components: N of reco track vs e, followed by a series of detector modules labeled DOM, TT, MS, and Unit 1 to Unit 6.



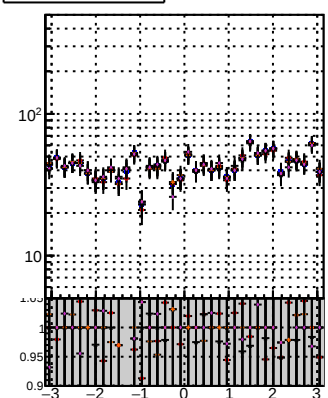
ut
pt
et



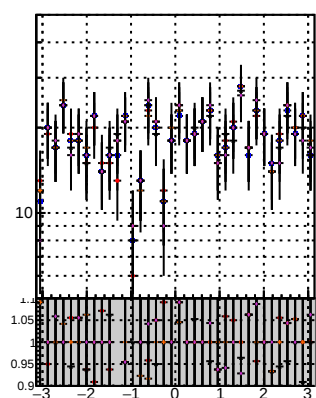
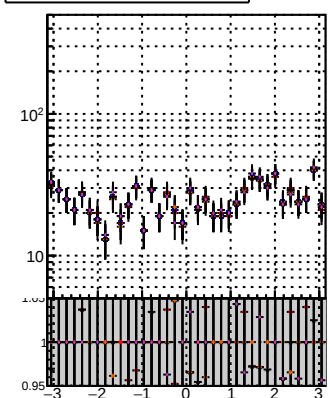
vs eta



N of reco track vs phi



N of associated (recoToSim) tracks vs phi



N of associated (recoToSim) duplicate tracks vs phi

