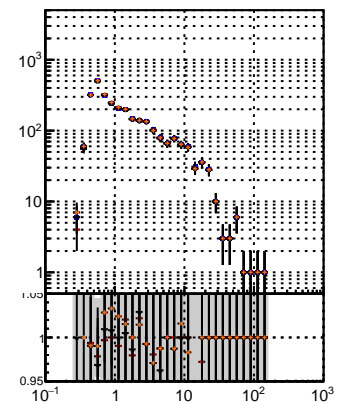
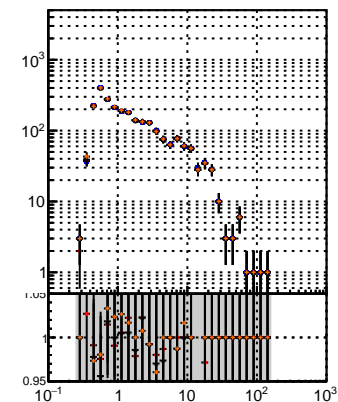


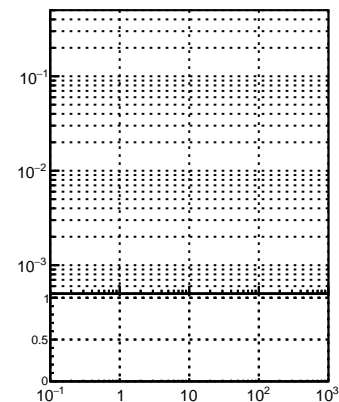
N of reco track vs pT



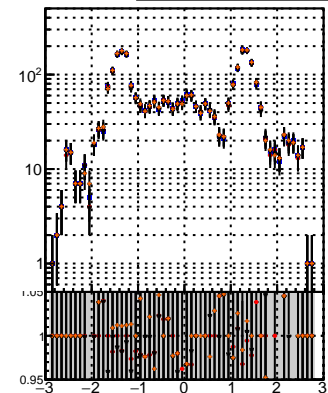
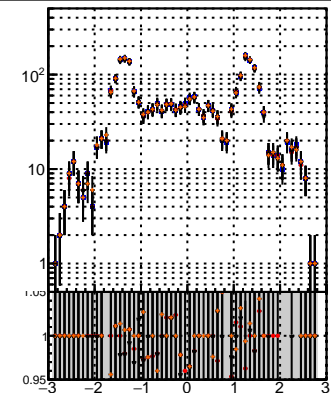
N of associated (recoToSim) tracks vs pT

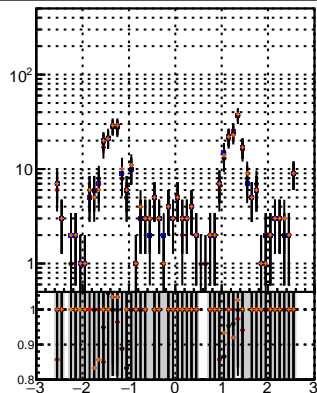


N of associated (recoToSim) duplicate tracks vs pT

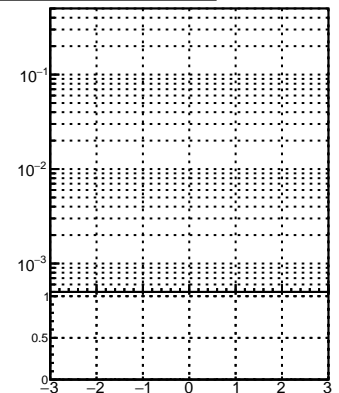


The diagram illustrates the detector layout for the N of reco track vs e. It shows a series of components: DQM, TT, MS, detT, detL1, and detTb9. The components are arranged in a row, with DQM, TT, and MS being the largest. The components are labeled with their respective names and are connected by lines. The diagram is a schematic representation of the detector layout.

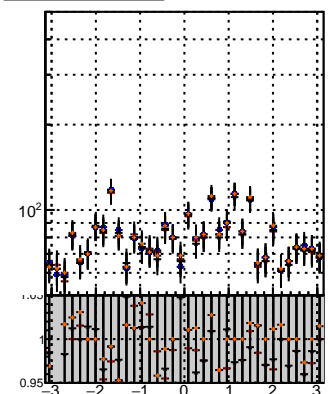
etT
etL1
etT09



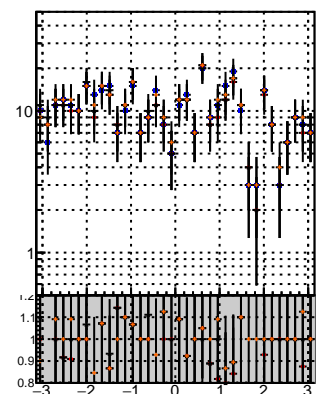
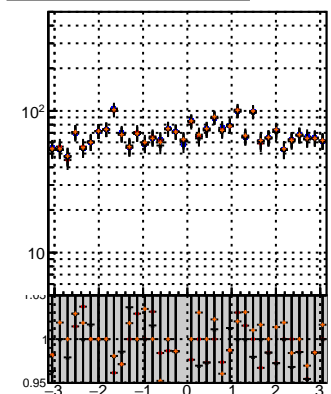
vs eta



N of reco track vs phi



N of associated (recoToSim) tracks vs phi



N of associated (recoToSim) duplicate tracks vs phi

