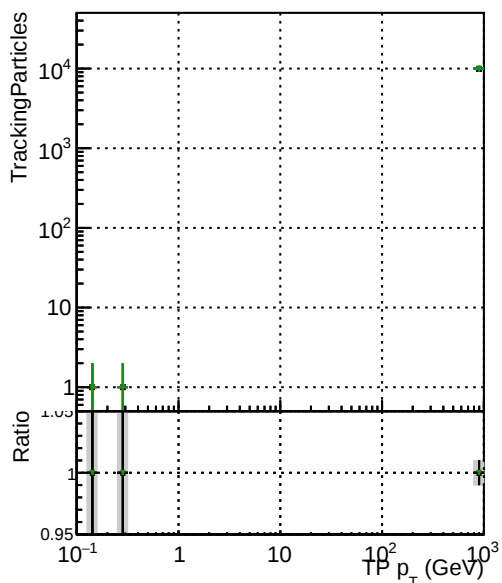
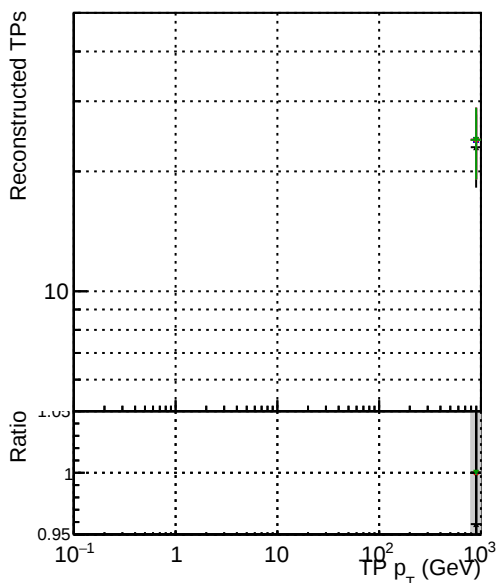


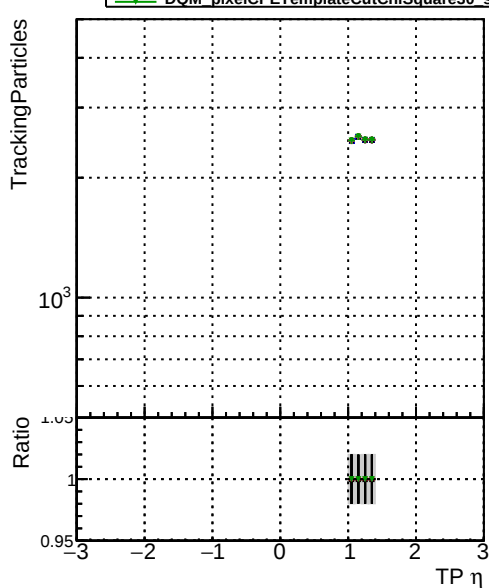
N of simulated tracks vs pT



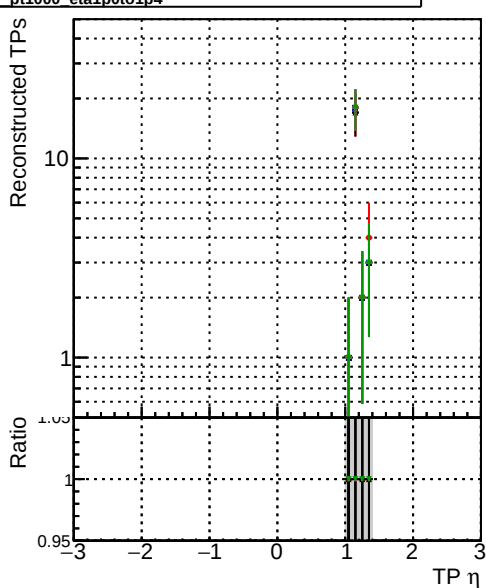
N of associated tracks (simToReco) vs pT



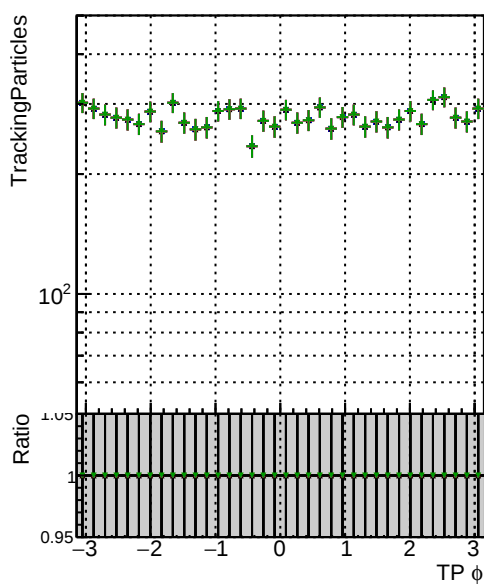
N of sim	DOM	pixel	CPU	mem
100	100	100	100	100
1000	1000	1000	1000	1000
10000	10000	10000	10000	10000



pt1000-eta1b0to1b4
pt1000-eta1b0to1b4
pt1000-eta1b0to1b4



N of simulated tracks vs phi



N of associated tracks (simToReco) vs phi

