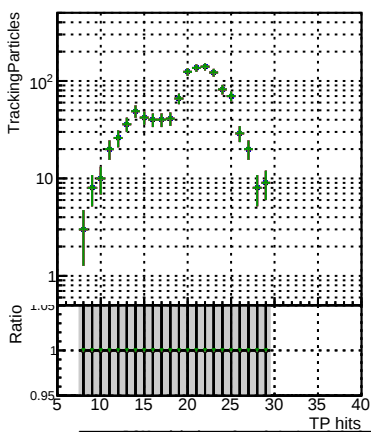
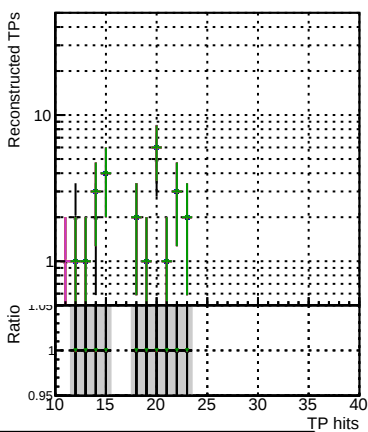


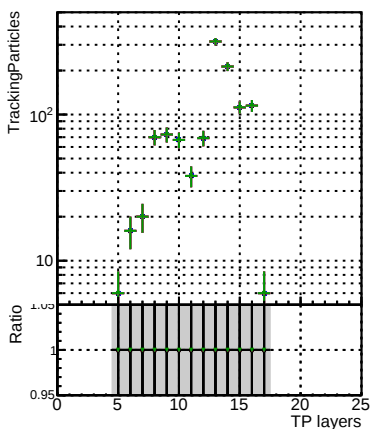
| N of simulated tracks vs hit |     |
|------------------------------|-----|
| 0                            | 0   |
| 1                            | 1   |
| 2                            | 2   |
| 3                            | 3   |
| 4                            | 4   |
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| 93                           | 93  |
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| 95                           | 95  |
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| 97                           | 97  |
| 98                           | 98  |
| 99                           | 99  |
| 100                          | 100 |



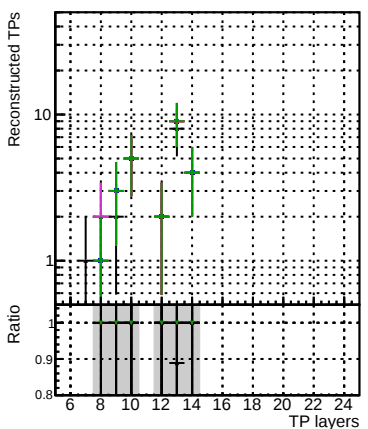
**N of associated tracks (simToReco) vs hit**



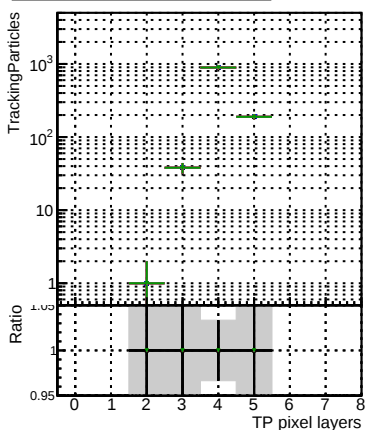
| N of sim | DOM pixelCPETemplateC | DOM pixelCPETemplateC | DOM pixelCPETemplateC |
|----------|-----------------------|-----------------------|-----------------------|
| 100      | 0.0000                | 0.0000                | 0.0000                |
| 200      | 0.0000                | 0.0000                | 0.0000                |
| 300      | 0.0000                | 0.0000                | 0.0000                |
| 400      | 0.0000                | 0.0000                | 0.0000                |
| 500      | 0.0000                | 0.0000                | 0.0000                |
| 600      | 0.0000                | 0.0000                | 0.0000                |
| 700      | 0.0000                | 0.0000                | 0.0000                |
| 800      | 0.0000                | 0.0000                | 0.0000                |
| 900      | 0.0000                | 0.0000                | 0.0000                |
| 1000     | 0.0000                | 0.0000                | 0.0000                |
| 1100     | 0.0000                | 0.0000                | 0.0000                |
| 1200     | 0.0000                | 0.0000                | 0.0000                |
| 1300     | 0.0000                | 0.0000                | 0.0000                |
| 1400     | 0.0000                | 0.0000                | 0.0000                |
| 1500     | 0.0000                | 0.0000                | 0.0000                |
| 1600     | 0.0000                | 0.0000                | 0.0000                |
| 1700     | 0.0000                | 0.0000                | 0.0000                |
| 1800     | 0.0000                | 0.0000                | 0.0000                |
| 1900     | 0.0000                | 0.0000                | 0.0000                |
| 2000     | 0.0000                | 0.0000                | 0.0000                |
| 2100     | 0.0000                | 0.0000                | 0.0000                |
| 2200     | 0.0000                | 0.0000                | 0.0000                |
| 2300     | 0.0000                | 0.0000                | 0.0000                |
| 2400     | 0.0000                | 0.0000                | 0.0000                |
| 2500     | 0.0000                | 0.0000                | 0.0000                |
| 2600     | 0.0000                | 0.0000                | 0.0000                |
| 2700     | 0.0000                | 0.0000                | 0.0000                |
| 2800     | 0.0000                | 0.0000                | 0.0000                |
| 2900     | 0.0000                | 0.0000                | 0.0000                |
| 3000     | 0.0000                | 0.0000                | 0.0000                |
| 3100     | 0.0000                | 0.0000                | 0.0000                |
| 3200     | 0.0000                | 0.0000                | 0.0000                |
| 3300     | 0.0000                | 0.0000                | 0.0000                |
| 3400     | 0.0000                | 0.0000                | 0.0000                |
| 3500     | 0.0000                | 0.0000                | 0.0000                |
| 3600     | 0.0000                | 0.0000                | 0.0000                |
| 3700     | 0.0000                | 0.0000                | 0.0000                |
| 3800     | 0.0000                | 0.0000                | 0.0000                |
| 3900     | 0.0000                | 0.0000                | 0.0000                |
| 4000     | 0.0000                | 0.0000                | 0.0000                |
| 4100     | 0.0000                | 0.0000                | 0.0000                |
| 4200     | 0.0000                | 0.0000                | 0.0000                |
| 4300     | 0.0000                | 0.0000                | 0.0000                |
| 4400     | 0.0000                | 0.0000                | 0.0000                |
| 4500     | 0.0000                | 0.0000                | 0.0000                |
| 4600     | 0.0000                | 0.0000                | 0.0000                |
| 4700     | 0.0000                | 0.0000                | 0.0000                |
| 4800     | 0.0000                | 0.0000                | 0.0000                |
| 4900     | 0.0000                | 0.0000                | 0.0000                |
| 5000     | 0.0000                | 0.0000                | 0.0000                |
| 5100     | 0.0000                | 0.0000                | 0.0000                |
| 5200     | 0.0000                | 0.0000                | 0.0000                |
| 5300     | 0.0000                | 0.0000                | 0.0000                |
| 5400     | 0.0000                | 0.0000                | 0.0000                |
| 5500     | 0.0000                | 0.0000                | 0.0000                |
| 5600     | 0.0000                | 0.0000                | 0.0000                |
| 5700     | 0.0000                | 0.0000                | 0.0000                |
| 5800     | 0.0000                | 0.0000                | 0.0000                |
| 5900     | 0.0000                | 0.0000                | 0.0000                |
| 6000     | 0.0000                | 0.0000                | 0.0000                |
| 6100     | 0.0000                | 0.0000                | 0.0000                |
| 6200     | 0.0000                | 0.0000                | 0.0000                |
| 6300     | 0.0000                | 0.0000                | 0.0000                |
| 6400     | 0.0000                | 0.0000                | 0.0000                |
| 6500     | 0.0000                | 0.0000                | 0.0000                |
| 6600     | 0.0000                | 0.0000                | 0.0000                |
| 6700     | 0.0000                | 0.0000                | 0.0000                |
| 6800     | 0.0000                | 0.0000                | 0.0000                |
| 6900     | 0.0000                | 0.0000                | 0.0000                |
| 7000     | 0.0000                | 0.0000                | 0.0000                |
| 7100     | 0.0000                | 0.0000                | 0.0000                |
| 7200     | 0.0000                | 0.0000                | 0.0000                |
| 7300     | 0.0000                | 0.0000                | 0.0000                |
| 7400     | 0.0000                | 0.0000                | 0.0000                |
| 7500     | 0.0000                | 0.0000                | 0.0000                |
| 7600     | 0.0000                | 0.0000                | 0.0000                |
| 7700</   |                       |                       |                       |



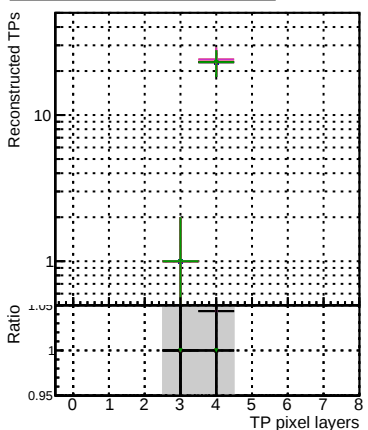
pt0p1to1\_eta2p0to2p5  
pt0p1to1\_eta2p0to2p5  
pt0p1to1\_eta2p0to2p5



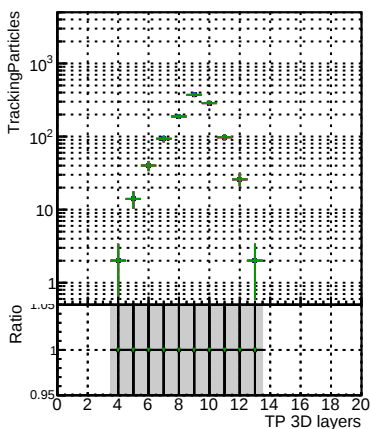
**N of simulated tracks vs pixel layer**



N of associated tracks (simToReco) vs pixel layer



**N of simulated tracks vs 3D layer**



N of associated tracks (simToReco) vs 3D layer

