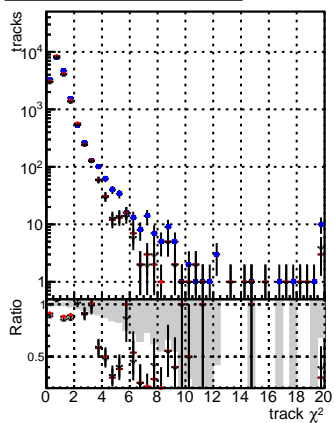
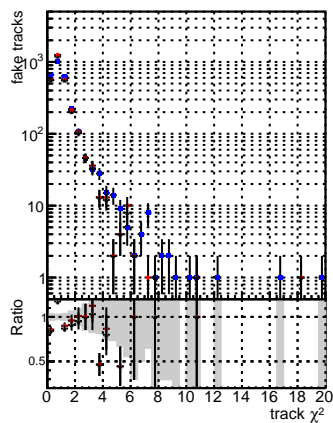
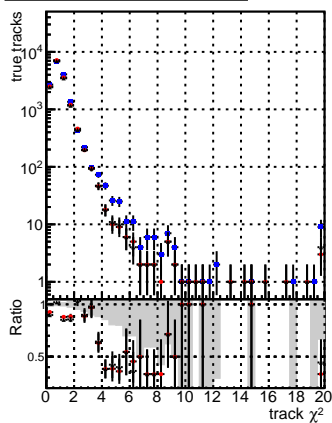


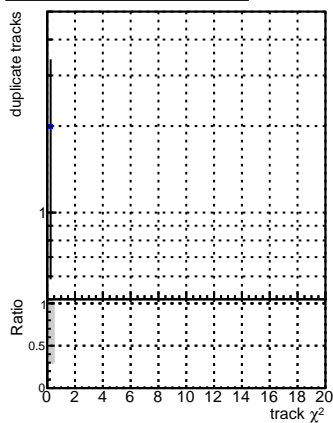
N of reco track vs normalized  $\chi^2$



N of associated (recoToSim) tracks vs normalized  $\chi^2$

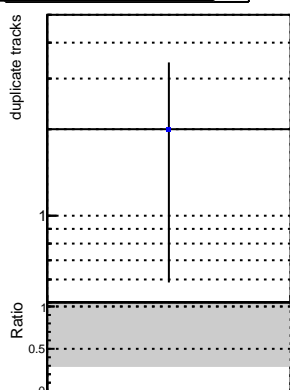
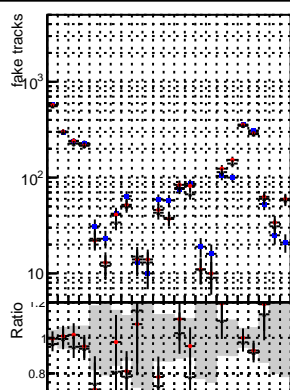
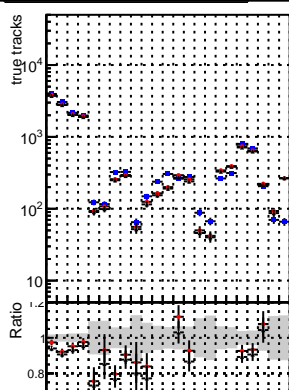
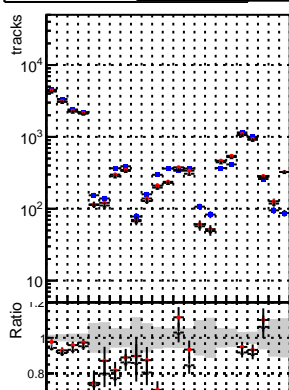


N of associated (recoToSim) loop tracks vs normalized  $\chi^2$



N of reco track vs.  $\chi^2$

DCM_V0001_R0000000000	Global_CMSSW_chiPU50_RECO
DCM_V0001_R0000000000	Global_CMSSW_chiPU50_lowPtQuad_RECO
DCM_V0001_R0000000000	Global_CMSSW_LowPtQnoclean_RECO



verSet