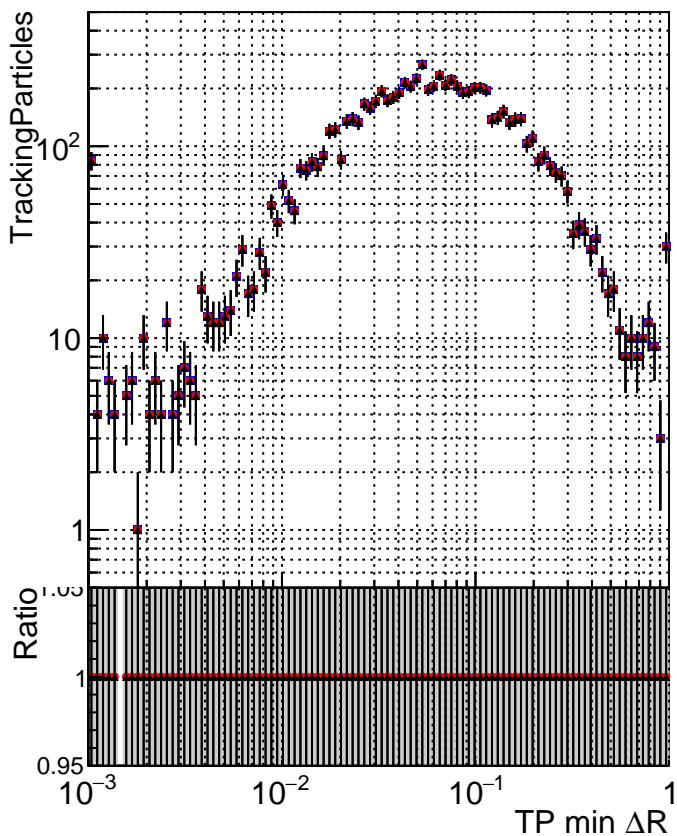
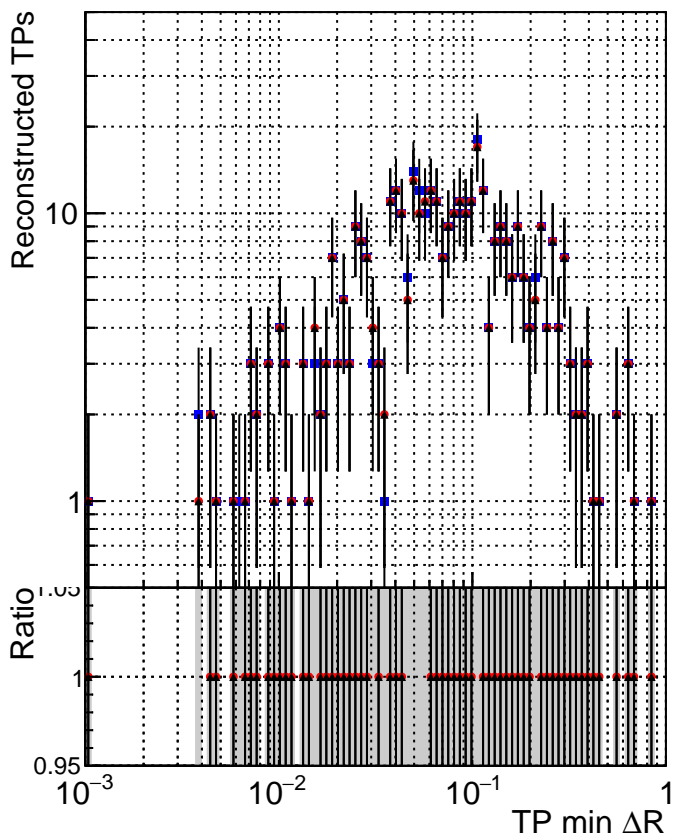


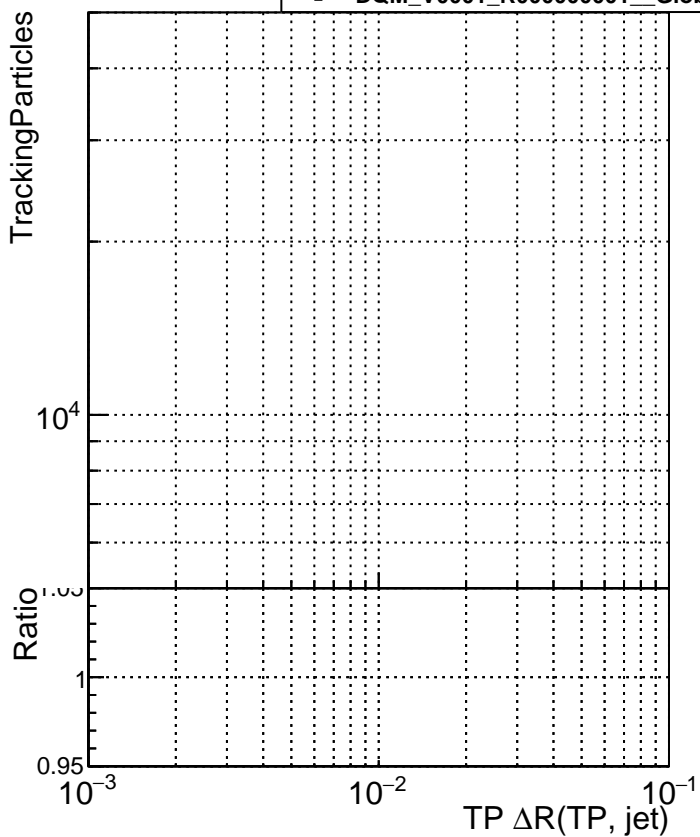
N of simulated tracks vs dR



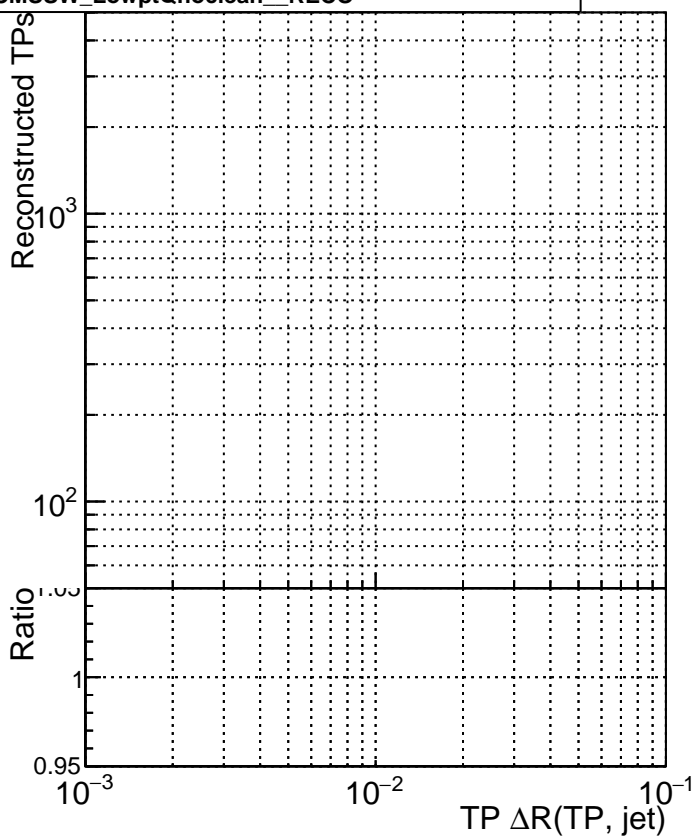
N of associated tracks (simToReco) vs dR



N of simulated tracks vs ΔR



N of associated tracks (simToReco) vs ΔR



- DQM_V0001_R000000001_Global_CMSSW_ckfPU50_RECO
- DQM_V0001_R000000001_Global_CMSSW_mkFitPU50LowPtQuad_RECO
- ▲ DQM_V0001_R000000001_Global_CMSSW_LowptQnoclean_RECO