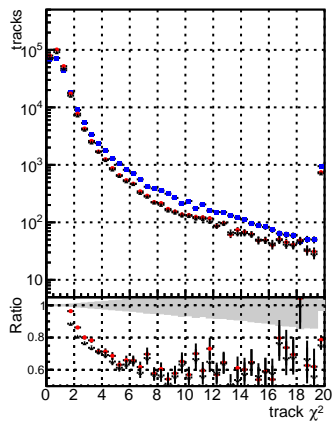
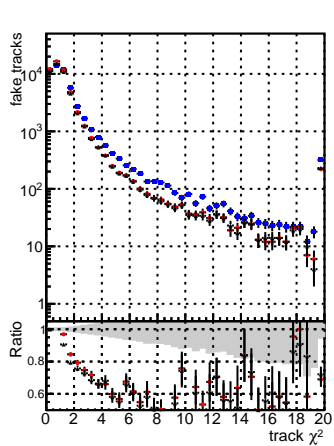
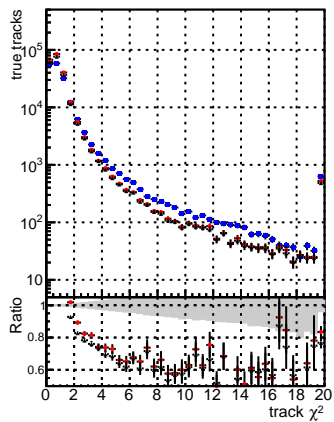


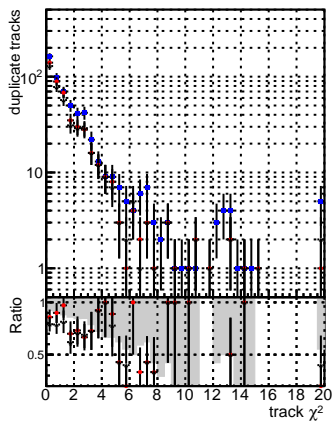
N of reco track vs normalized  $\chi^2$



N of associated (recoToSim) tracks vs normalized  $\chi^2$



N of associated (recoToSim) loopier tracks vs normalized  $\chi^2$



N of reco track vs.  $\chi^2$

Global CMSSW chip50_RECO	Global CMSSW chip50_RECO	Global CMSSW chip50_RECO
Global CMSSW chip50_RECO	Global CMSSW chip50_RECO	Global CMSSW chip50_RECO
Global CMSSW chip50_RECO	Global CMSSW chip50_RECO	Global CMSSW chip50_RECO

