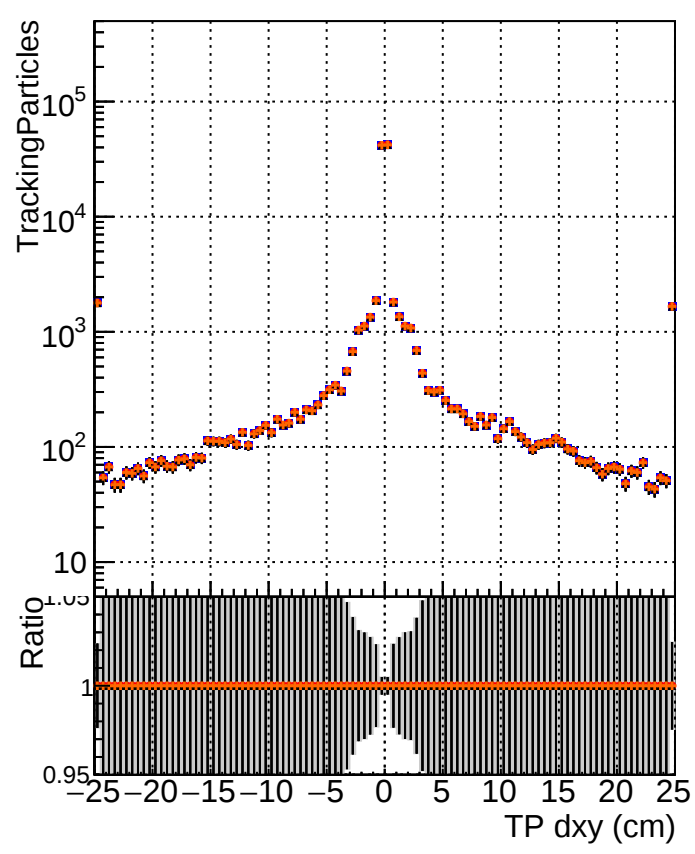


N of simulated tracks vs dxy



N of associated tracks (simToReco) vs dxy



N of simulated track

■	DQM_V0001_R000000001	Global	CMSSW_ckfPU50	RECO
●	DQM_V0001_R000000001	Global	CMSSW_mkFitPU50LowPtQuad	RECO
▲	DQM_V0001_R000000001	Global	CMSSW_LowPtQnoclean	RECO
◆	DQM_V0001_R000000001	Global	CMSSW_looseSC	RECO

