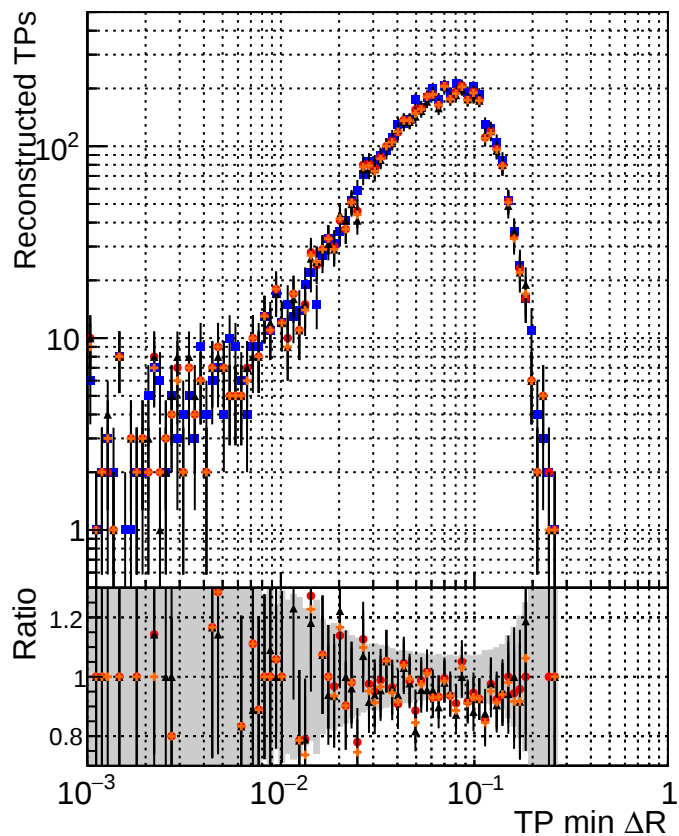


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated tracks

■	DQM_V0001_R000000001	Global	CMSSW_ck1PU50	RECO
●	DQM_V0001_R000000001	Global	CMSSW_mkFitPU50LowPtQuad	RECO
▲	DQM_V0001_R000000001	Global	CMSSW_LowPtQnoclean	RECO
+	DQM_V0001_R000000001	Global	CMSSW_looseSC	RECO

