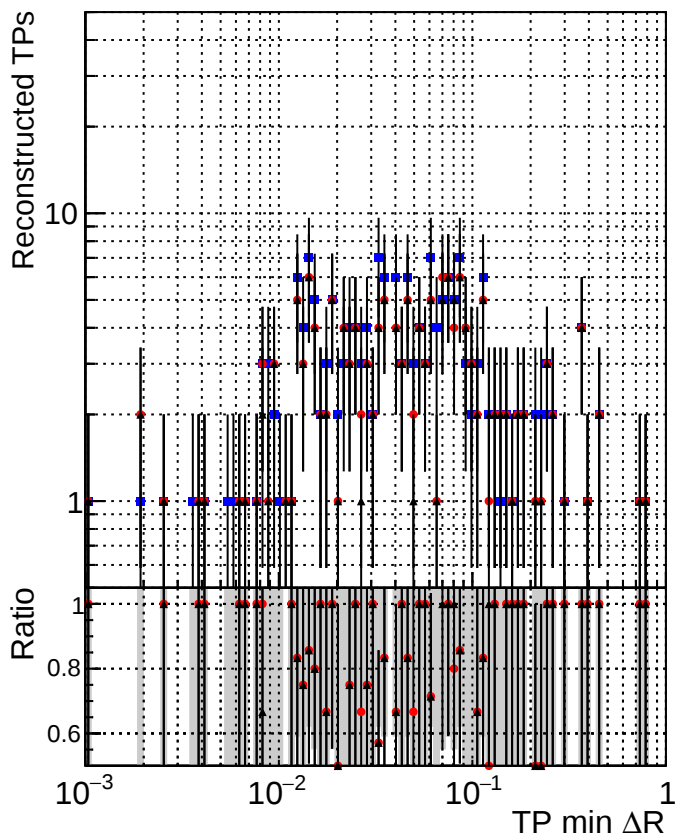


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated tracks

■ DQM\_V0001\_R000000001\_Global\_CMSSW\_ckfPU50\_RECO  
● DQM\_V0001\_R000000001\_Global\_CMSSW\_mkFitPU50LowPtQuad\_RECO  
▲ DQM\_V0001\_R000000001\_Global\_CMSSW\_LowptQnoclean\_RECO

