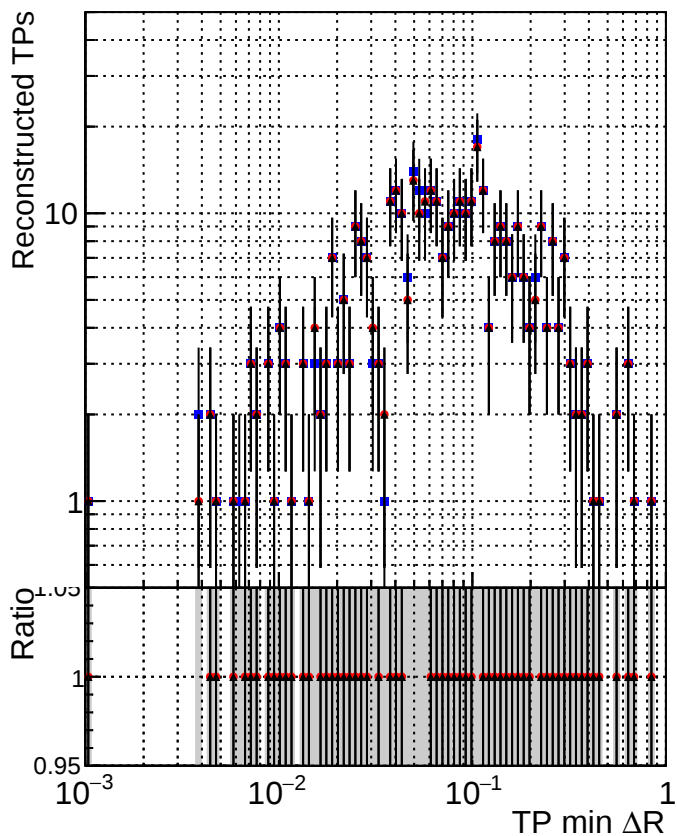


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated tracks

■ DQM_V0001_R000000001 Global CMSSW_ckfPU50 RECO
● DQM_V0001_R000000001 Global CMSSW_mkFitPU50LowPtQuad RECO
▲ DQM_V0001_R000000001 Global CMSSW_LowptQnoclean RECO

