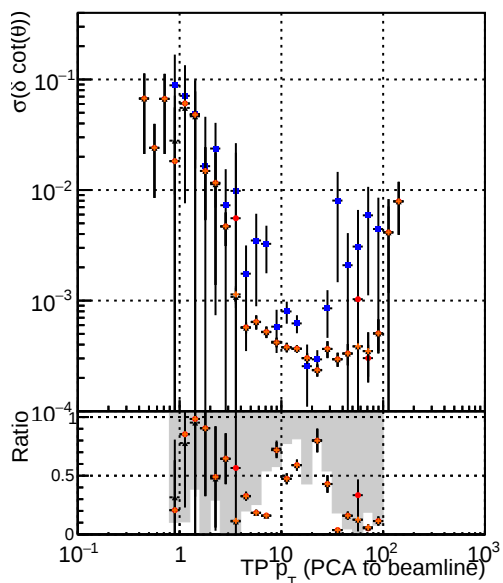
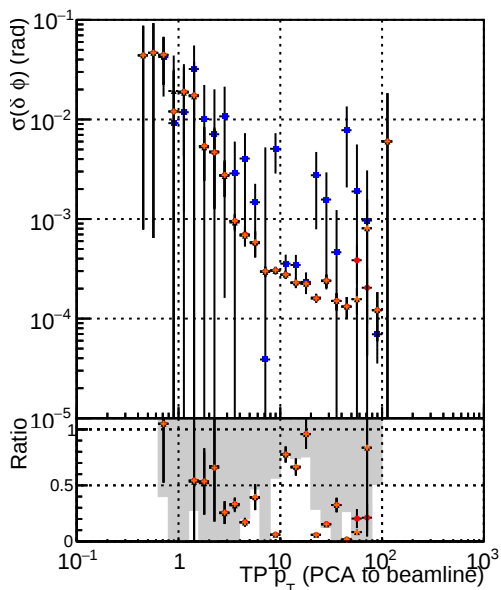




—•—	DQM_V0001_R0000000001_Global_CMSSW_cktPU50_RECO
—•—	DQM_V0001_R0000000001_Global_CMSSW_mkFitPU50LowPtQuad_RECO
—•—	DQM_V0001_R0000000001_Global_CMSSW_LowptQnoSC_RECO
—•—	DQM_V0001_R0000000001_Global_CMSSW_LowptQnoDC_RECO

