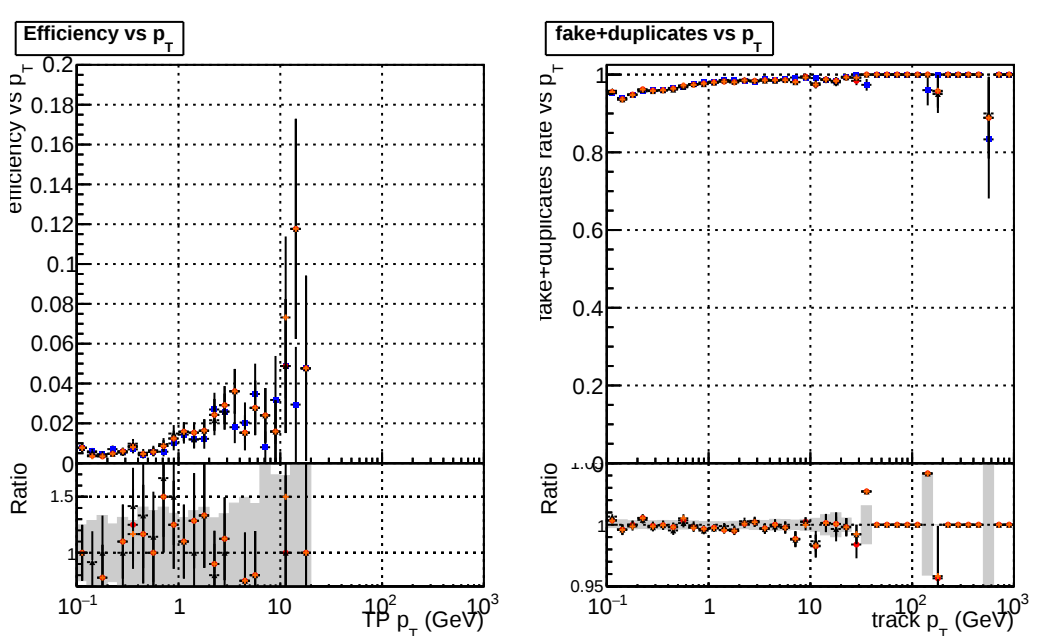




—•—	DQM_V0001_R0000000001	Global	CMSSW_ckitPU50_RECO
—•—	DQM_V0001_R0000000001	Global	CMSSW_mkFitPU50LowPtQuad_RECO
—•—	DQM_V0001_R0000000001	Global	CMSSW_LowptQnoSC_RECO
—•—	DQM_V0001_R0000000001	Global	CMSSW_LowptQnoDC_RECO

