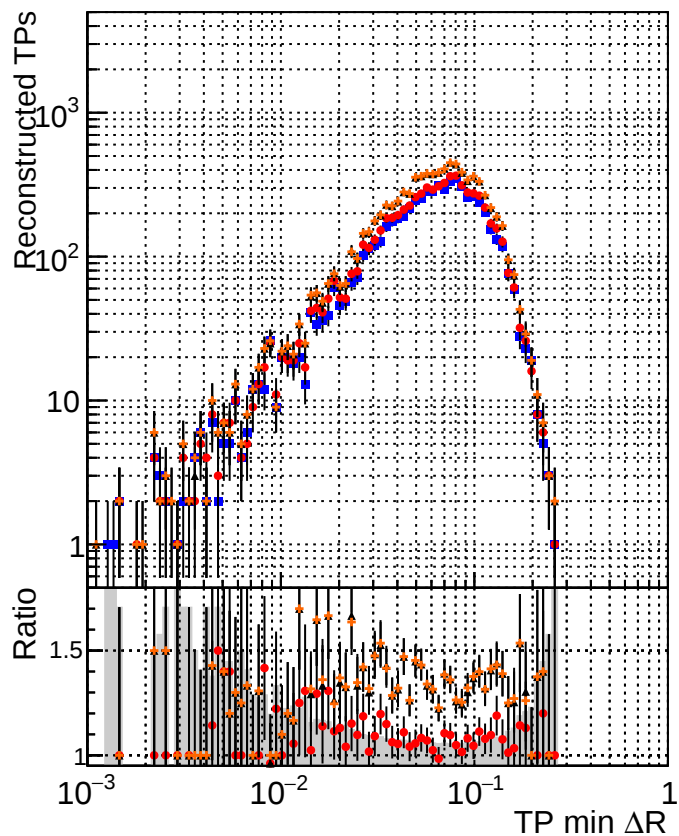


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated tracks

DQM_V0001_R000000001	Global	CMSSW_ckptPU50_RECO
DQM_V0001_R000000001	Global	CMSSW_mkFitPU50_RECO
DQM_V0001_R000000001	Global	CMSSW_mkFitPU50LowPtQuad_RECO
DQM_V0001_R000000001	Global	CMSSW_mkFitPU50LowPtQuad-noBkwd_RECO

