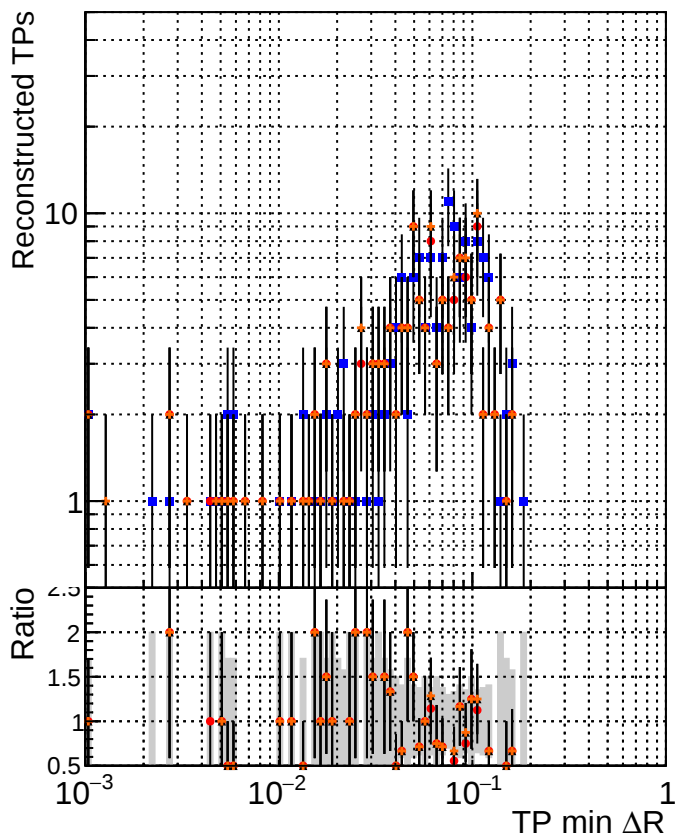


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated tracks

■	DQM_V0001_R0000000001_Global_CMSSW_ckptPU50_RECO
●	DQM_V0001_R0000000001_Global_CMSSW_mkFitPU50LowPtQuad_RECO
▲	DQM_V0001_R0000000001_Global_CMSSW_scm05_RECO
■	DQM_V0001_R0000000001_Global_CMSSW_scm05+dup_RECO

