

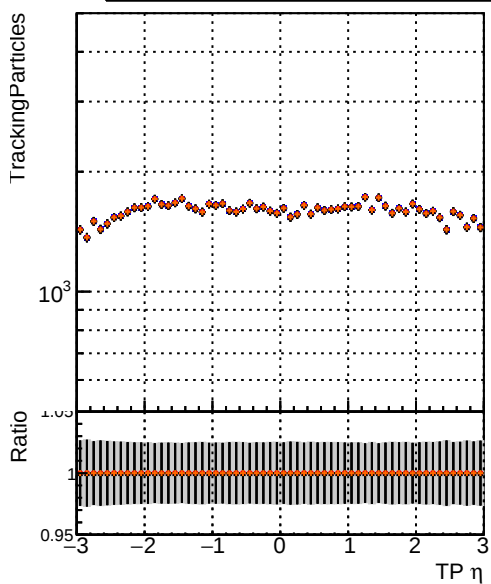
N of simulated tracks vs pT



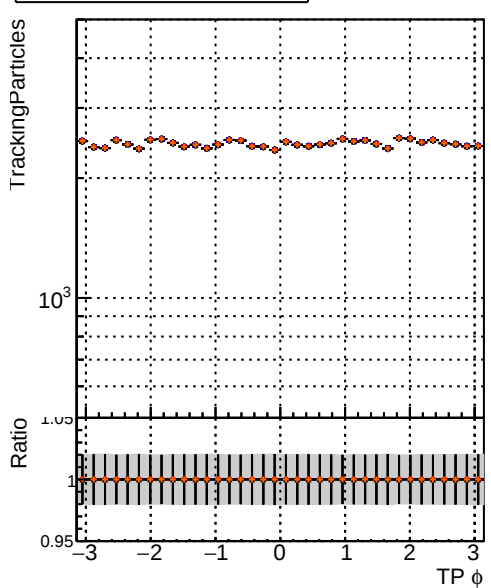
N of associated tracks (simToReco) vs pT



N of simulated tracks	Reco	Reco	Reco
DQM_V0001_R0000000001_Global_CMSSW_ckptPU50_RECO	Global_CMSSW_ckptPU50_RECO	Global_CMSSW_ckptPU50_RECO	Global_CMSSW_ckptPU50_RECO
DQM_V0001_R0000000001_Global_CMSSW_mkFitPU50LowPtQuad_RECO	Global_CMSSW_mkFitPU50LowPtQuad_RECO	Global_CMSSW_mkFitPU50LowPtQuad_RECO	Global_CMSSW_mkFitPU50LowPtQuad_RECO
DQM_V0001_R0000000001_Global_CMSSW_scm05_RECO	Global_CMSSW_scm05_RECO	Global_CMSSW_scm05_RECO	Global_CMSSW_scm05_RECO
DQM_V0001_R0000000001_Global_CMSSW_scm05+dup_RECO	Global_CMSSW_scm05+dup_RECO	Global_CMSSW_scm05+dup_RECO	Global_CMSSW_scm05+dup_RECO



N of simulated tracks vs phi



N of associated tracks (simToReco) vs phi

