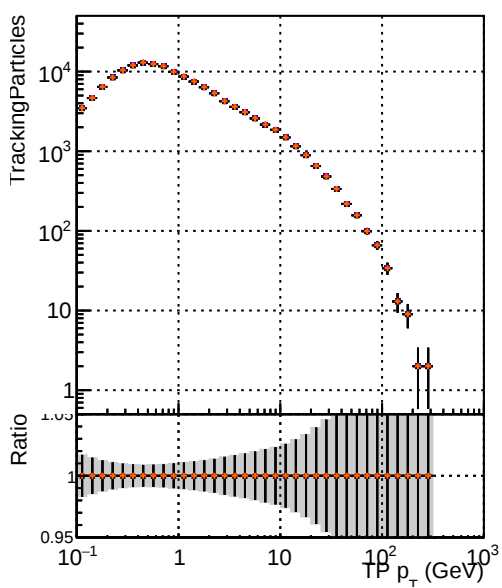
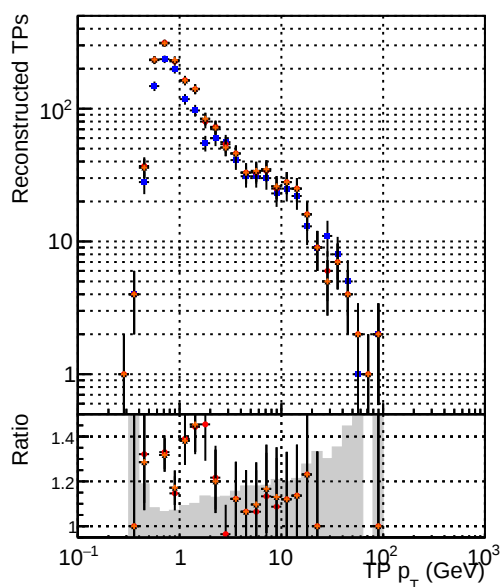


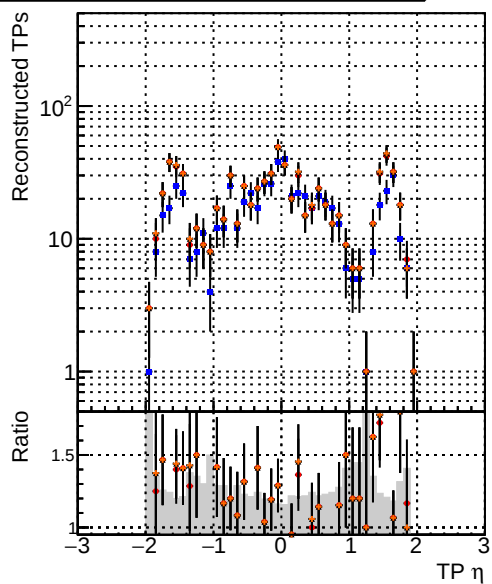
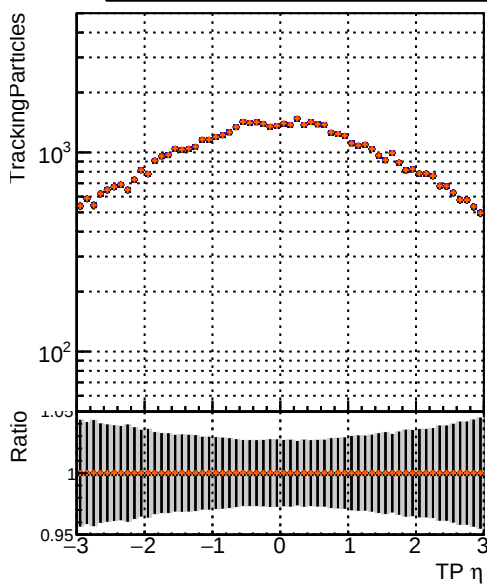
N of simulated tracks vs pT



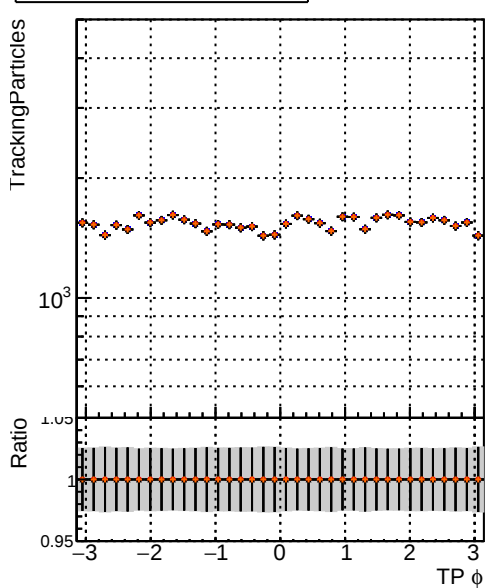
N of associated tracks (simToReco) vs pT



N of simulated tracks	Model	Global	CMSSW	ckitPU50	RECO
—	DQM_V0001_R0000000001	Global	CMSSW	ckitPU50	RECO
—	DQM_V0001_R0000000001	Global	CMSSW	mkFitPU50LowPtQuad	RECO
—	DQM_V0001_R0000000001	Global	CMSSW	scmod05	RECO
—	DQM_V0001_R0000000001	Global	CMSSW	scmod05+dup	RECO



N of simulated tracks vs phi



N of associated tracks (simToReco) vs phi

