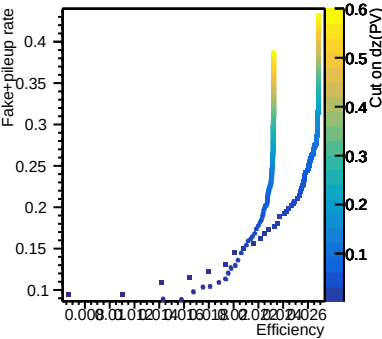
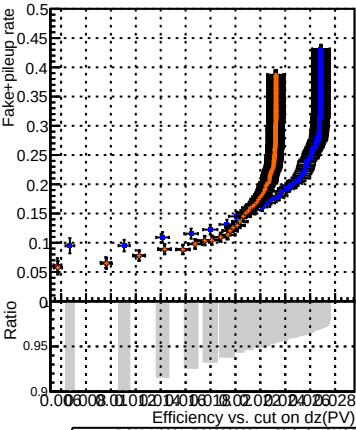




—+—	DQM_V0001_R000000001_Global_CMSSW_ckfPU50_RECO
—+—	DQM_V0001_R000000001_Global_CMSSW_mkFitPU50LowPtQuad_RECO
—+—	DQM_V0001_R000000001_Global_CMSSW_scm05_RECO
—+—	DQM_V0001_R000000001_Global_CMSSW_scm05+dup_RECO