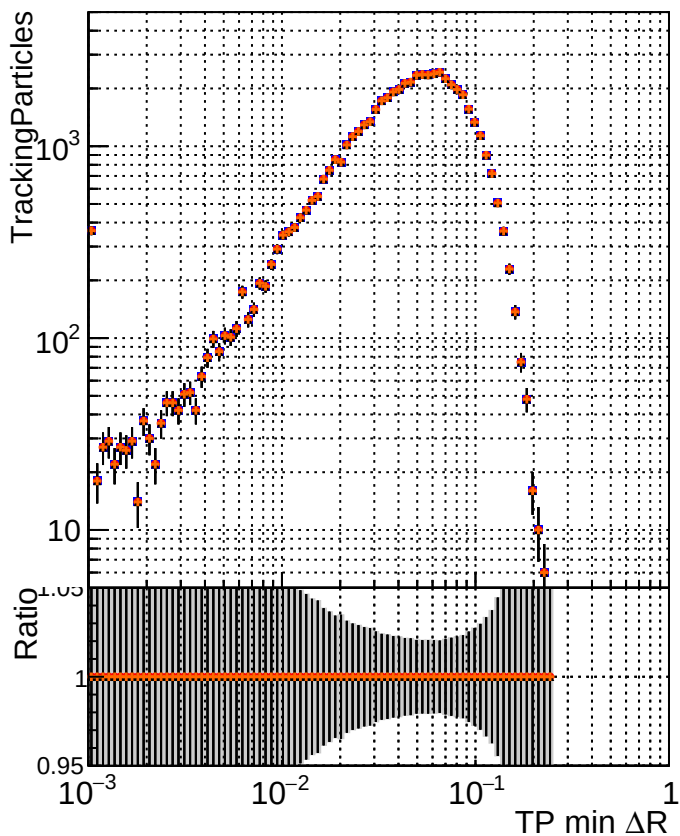
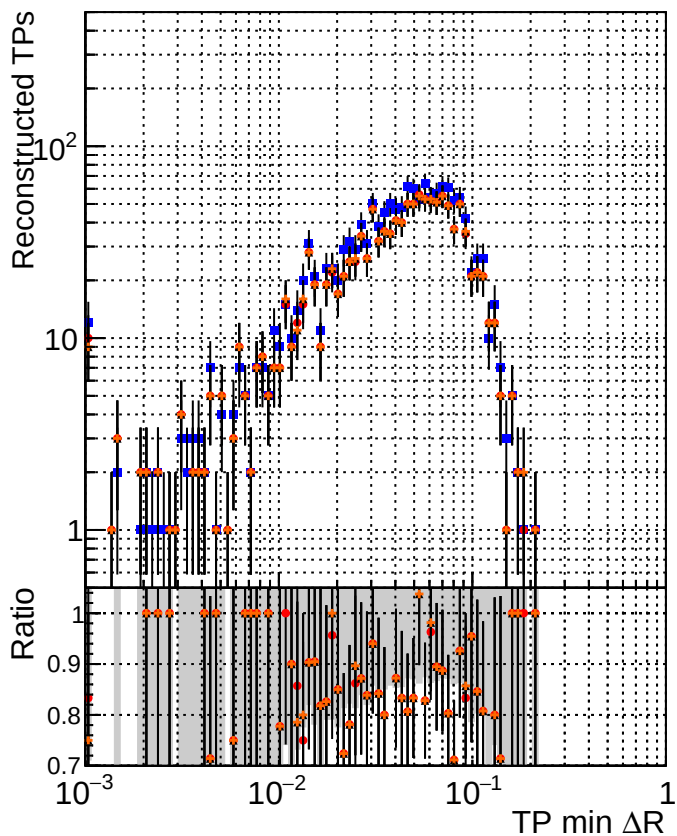


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated trac

	DQM_V0001_R0000000001	Global	CMSSW_ck1PU50	RECO
	DQM_V0001_R0000000001	Global	CMSSW_mkFitPU50LowPtQuad	RECO
	DQM_V0001_R0000000001	Global	CMSSW_scm05	RECO
	DQM_V0001_R0000000001	Global	CMSSW_scm05+dup	RECO

