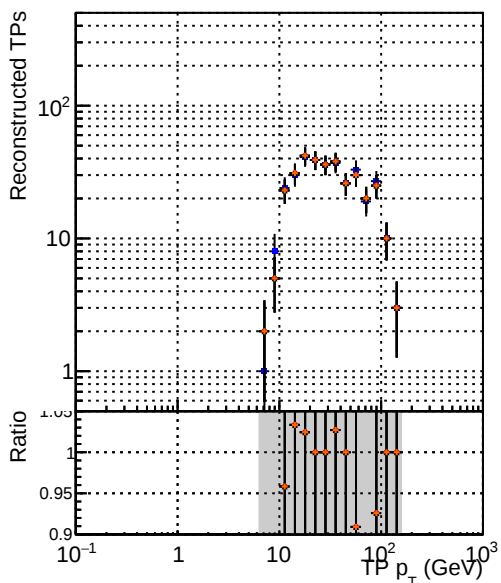


**N of simulated tracks vs pT**

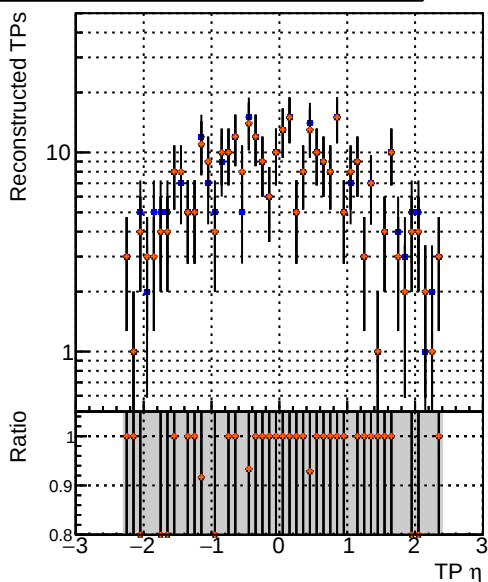
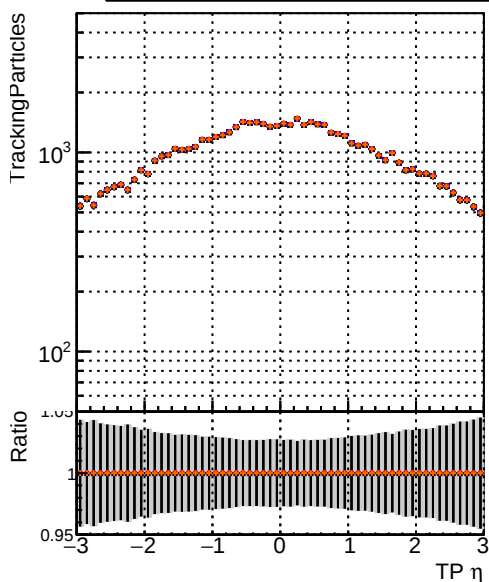


**N of associated tracks (simToReco) vs pT**

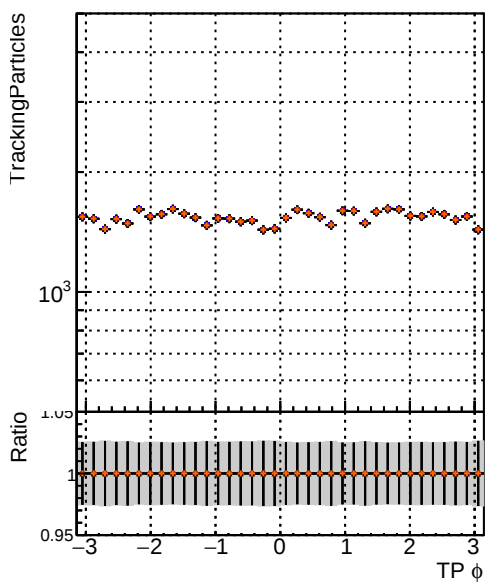


|     |                      |        |                               |
|-----|----------------------|--------|-------------------------------|
| —●— | DQM_V0001_R000000001 | Global | CMSSW_ckitPU50_RECO           |
| —●— | DQM_V0001_R000000001 | Global | CMSSW_mkFitPU50LowPtQuad_RECO |
| —●— | DQM_V0001_R000000001 | Global | CMSSW_scm05_RECO              |
| —●— | DQM_V0001_R000000001 | Global | CMSSW_scm05+dup_RECO          |

**N of simulated tracks vs phi**



**N of simulated tracks vs phi**



**N of associated tracks (simToReco) vs phi**

