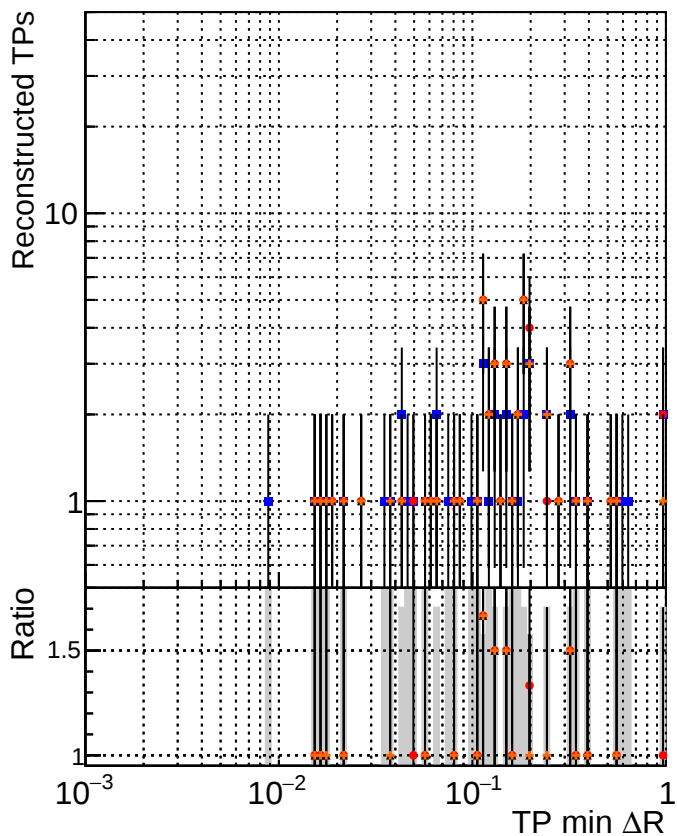


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated tracks

|   |                       |        |                               |
|---|-----------------------|--------|-------------------------------|
| ■ | DQM_V0001_R0000000001 | Global | CMSSW_ckfPU50_RECO            |
| ● | DQM_V0001_R0000000001 | Global | CMSSW_mkFitPU50LowPtQuad_RECO |
| ▲ | DQM_V0001_R0000000001 | Global | CMSSW_scm05_RECO              |
| ◆ | DQM_V0001_R0000000001 | Global | CMSSW_scm05+dup_RECO          |

