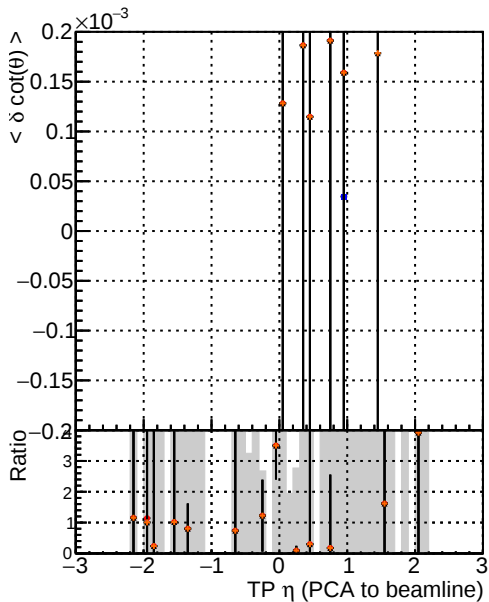
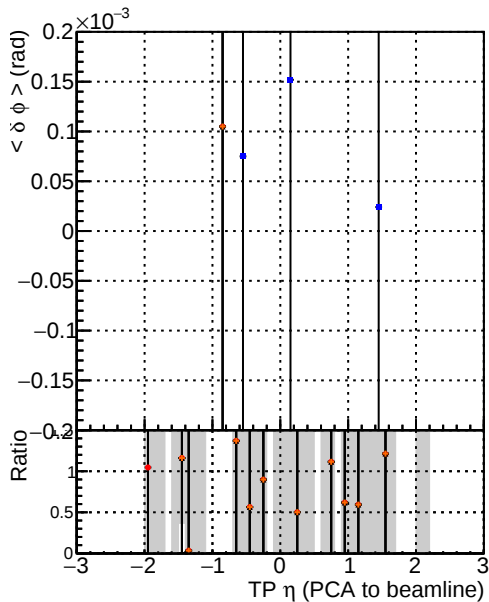




— DQM\_V0001\_R000000001\_Global\_CMSSW\_ckfPU50\_RECO  
 — DQM\_V0001\_R000000001\_Global\_CMSSW\_mkFitPU50LowPtQuad\_RECO  
 — DQM\_V0001\_R000000001\_Global\_CMSSW\_scm05\_RECO  
 — DQM\_V0001\_R000000001\_Global\_CMSSW\_scm05+dup\_RECO

