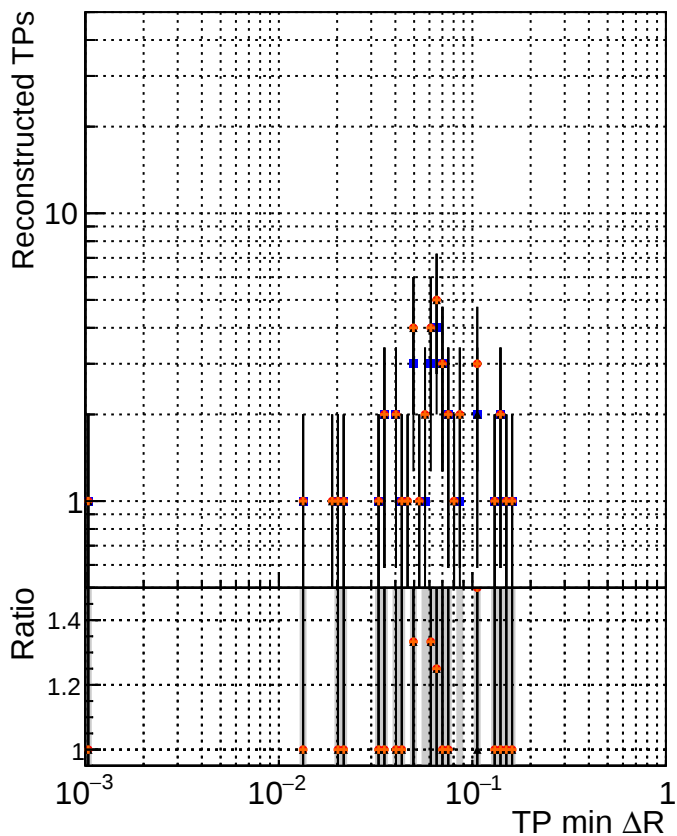


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated trac

■	DQM_V0001_R000000001_Global_CMSSW_ckFitPU50_RECO
●	DQM_V0001_R000000001_Global_CMSSW_mkFitPU50_RECO
▲	DQM_V0001_R000000001_Global_CMSSW_mkFitPU50LowPtQuad_RECO
■	DQM_V0001_R000000001_Global_CMSSW_mkFitPU50LowPtTriplet_RECO

