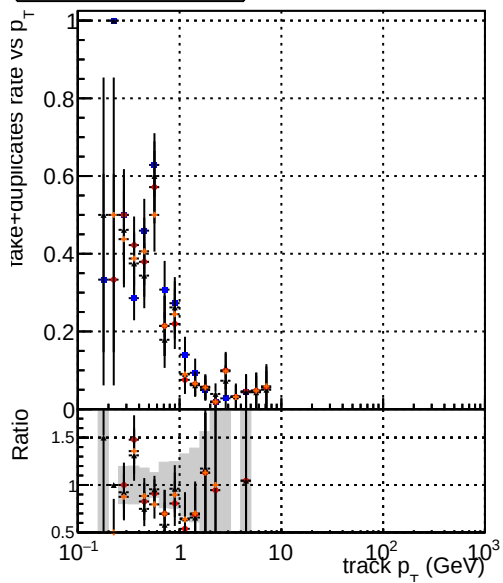
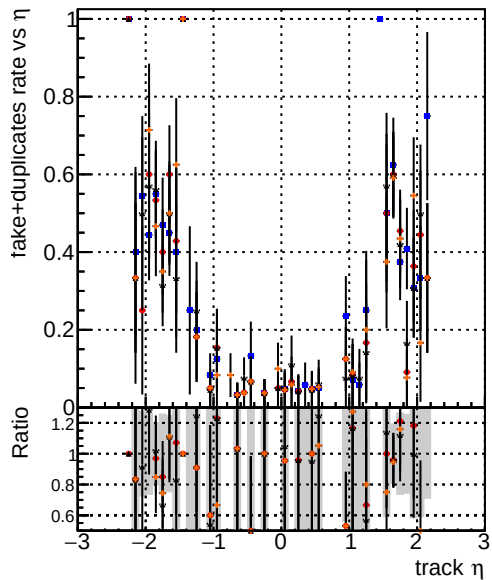


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—●—	DQM_V0001_R0000000001	Global	CMSSW_ckptPU50	RECO
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—●—	DQM_V0001_R0000000001	Global	CMSSW_mkFitPU50LowPtTriplet	RECO

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