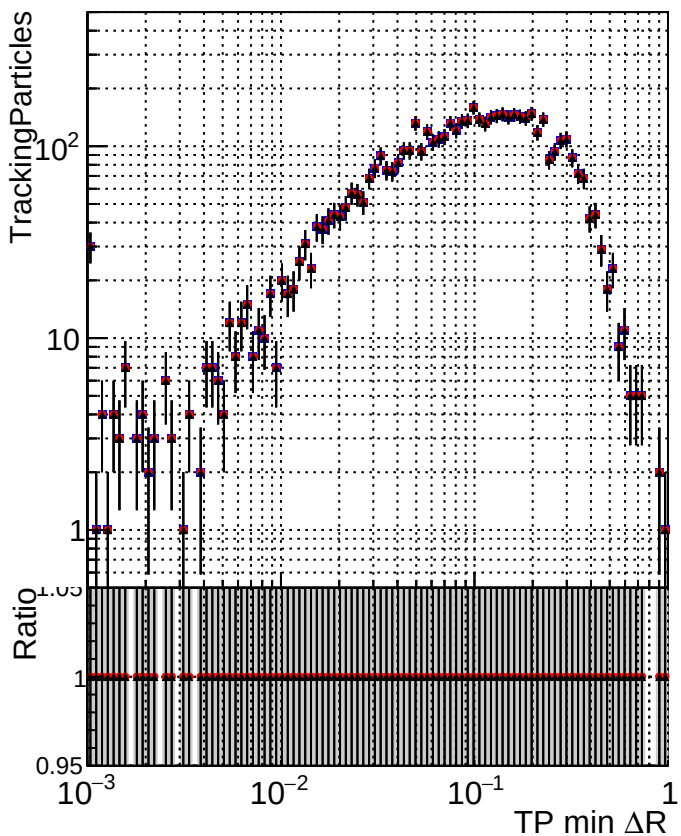
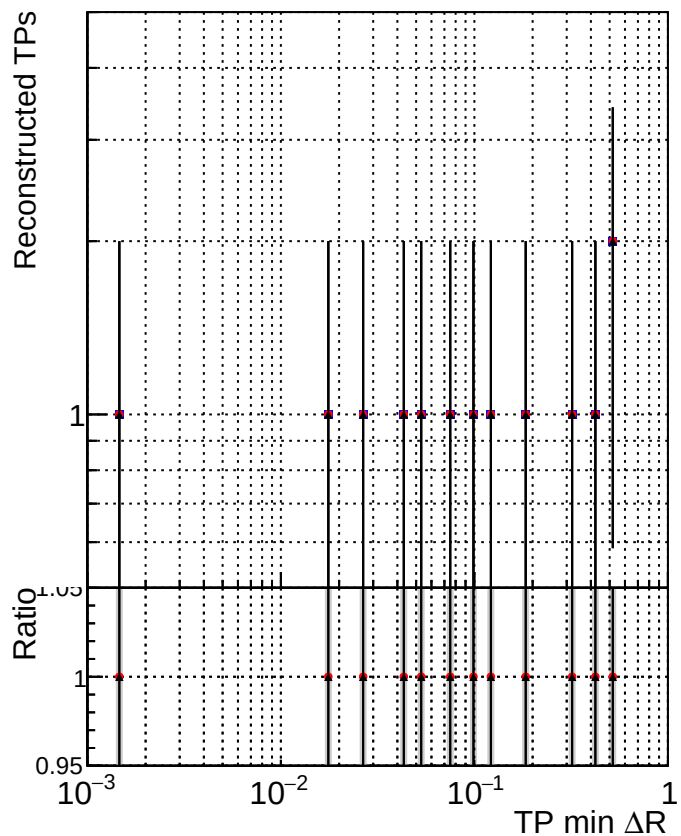


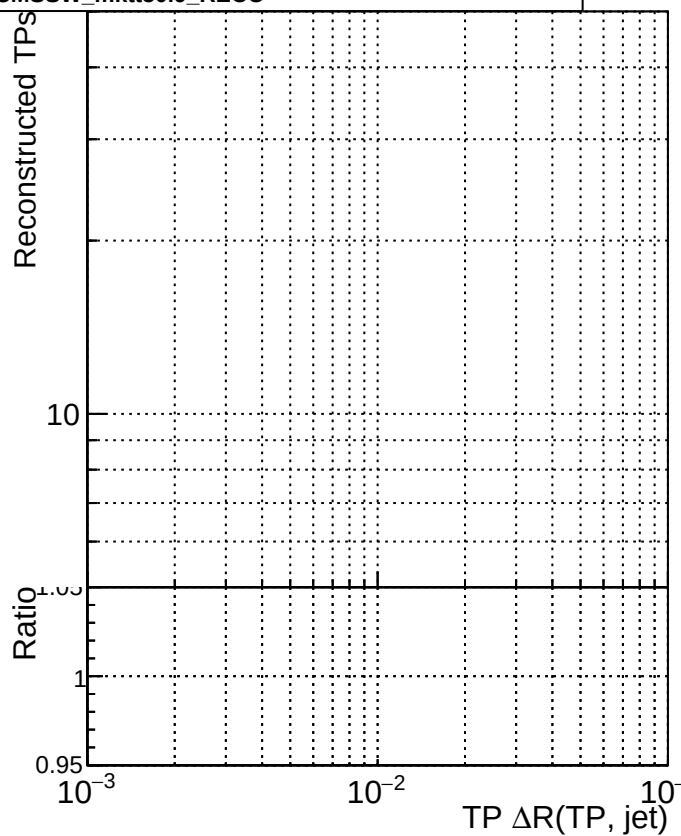
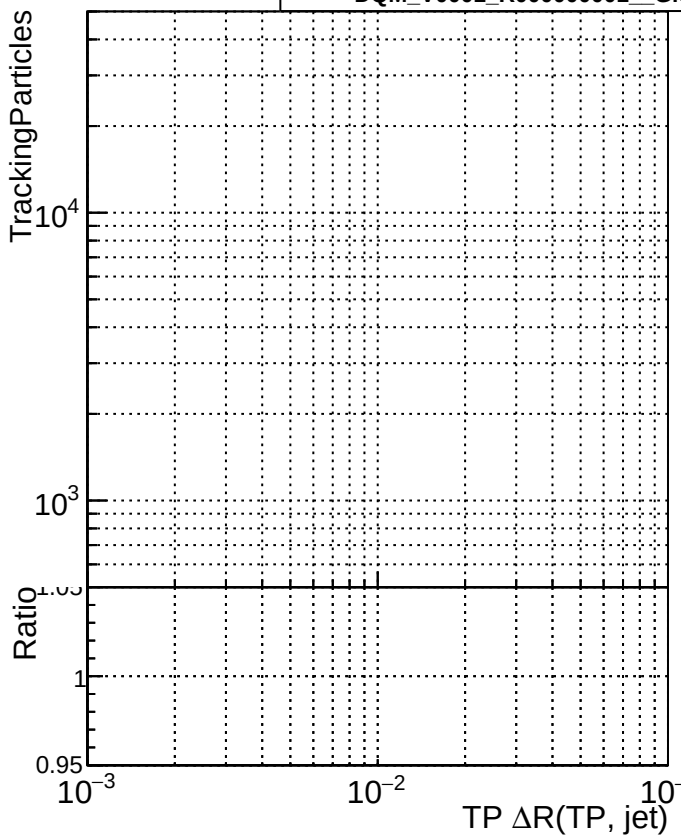
N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated trac



■	DQM_V0001_R000000001	Global	CMSSW_mkt50i7_RECO
●	DQM_V0001_R000000001	Global	CMSSW_mkt50i8_RECO
▲	DQM_V0001_R000000001	Global	CMSSW_mkt50i9_RECO