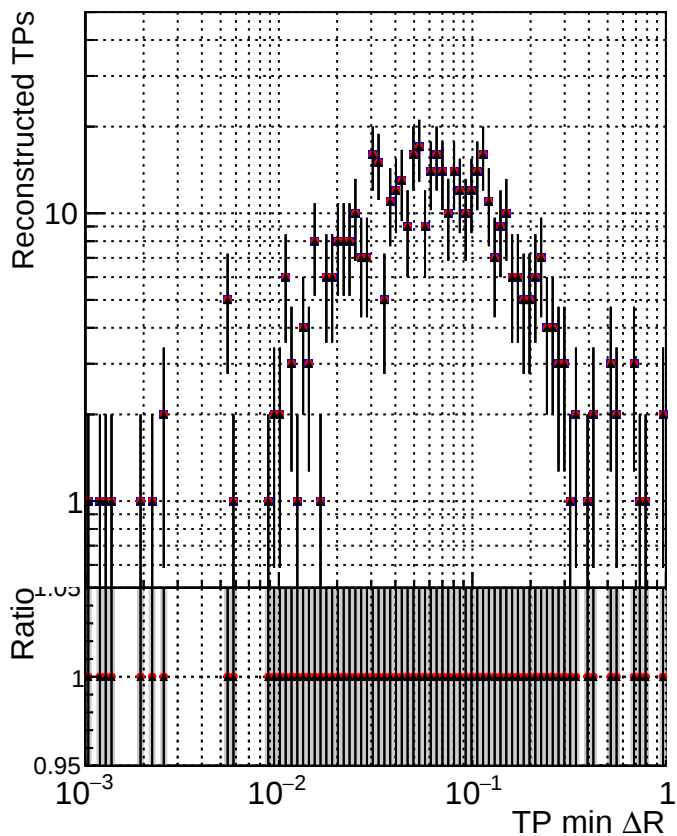


N of simulated tracks vs dR



N of associated tracks (simToReco) vs dR



N of simulated trac

■ DQM_V0001_R0000000001 Global CMSSW_mkt50i4_RECO
 ● DQM_V0001_R0000000001 Global CMSSW_mkt50i5_RECO
 ▲ DQM_V0001_R0000000001 Global CMSSW_mkt50i6_RECO

