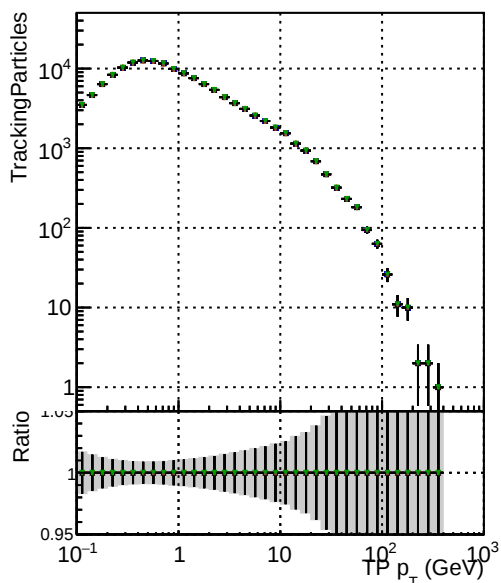


N of simulated tracks vs pT



N of associated tracks (simToReco) vs pT

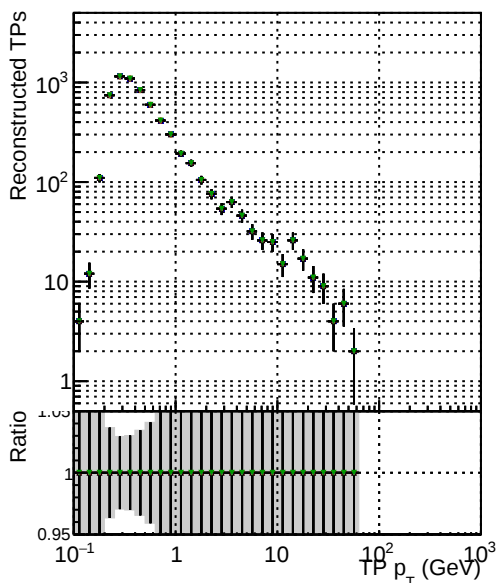
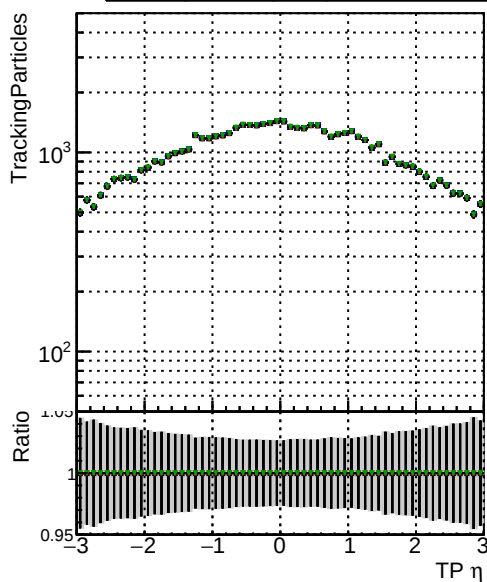
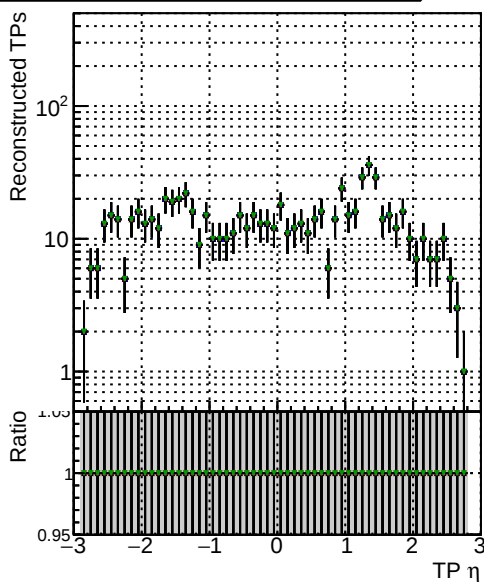


Diagram illustrating a signal bus structure. The bus is divided into two sections: DOM (Data Object Memory) and TT (Transaction Table). The bus is labeled "N of sin" on the left. The bus lines are labeled on the right as follows:

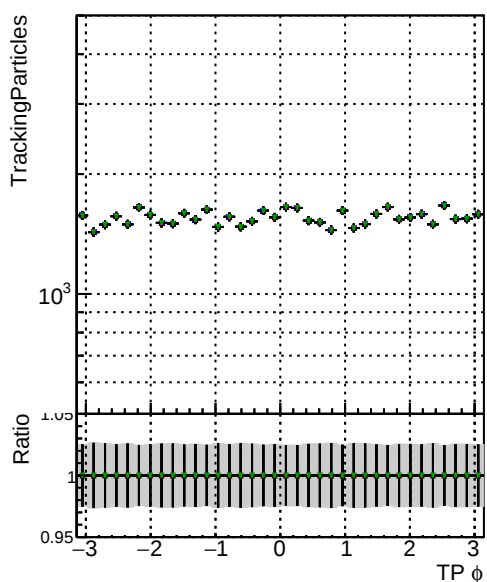
- original REDO
- signal option0
- signal option1
- signal option2
- original option0
- original option1

[illegible]

Sc



N of simulated tracks vs phi



N of associated tracks (simToReco) vs phi

